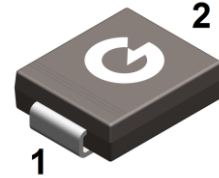


Features

- Metal silicon junction, majority carrier conduction
- High forward surge current capability
- Low forward voltage drop
- High temperature soldering guaranteed: 260°C @ 10s
- Robust guarding for over voltage protection
- RoHS compliant with Halogen-free
- Qualified to AEC-Q101 Standards

HF



SMC

Mechanical Data

- Case: SMC
- Molding compound: UL flammability classification rating 94-0
- Terminals: Tin-plated; solderability per MIL-STD-202, Method 208
- Polarity: Color band denotes cathode end

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
TSS5150C	SMC	3000 pcs / Tape & Reel	SS5CC
TSS5200C	SMC	3000 pcs / Tape & Reel	SS5DC

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	TSS5150C	TSS5200C	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	150	200	V
RMS Reverse Voltage	V _{RMS}	105	140	V
DC Blocking Voltage	V _{DC}	150	200	V
Average Forward Output Current	I _{F(AV)}	5.0		A
Peak Forward Surge Current, 8.3ms Single Half-Sine-Wave	I _{FSM}	150		A

Thermal Characteristics

Parameter	Symbol	TSS5150C	TSS5200C	Unit
Thermal Resistance Junction-to-Air ^{*1}	R _{θJA}	62		°C/W
Thermal Resistance Junction-to-Case ^{*1}	R _{θJC}	28		°C/W
Thermal Resistance Junction-to-Lead ^{*1}	R _{θJL}	20		°C/W
Operating Junction Temperature Range	T _J	-55 ~ +175		°C
Storage Temperature Range	T _{STG}	-55 ~ +175		°C

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Reverse Breakdown Voltage	$V_{(BR)}$	$I_R = 100\mu\text{A}$, TSS5150C	150	-	-	V
		$I_R = 100\mu\text{A}$, TSS5200C	200	-	-	V
Forward Voltage *2	V_F	$I_F = 5\text{A}$	-	-	0.85	V
Maximum Peak Reverse Current *3	I_R	Rated V_R , $T_J = 25^\circ\text{C}$	-	-	30	μA
		Rated V_R , $T_J = 100^\circ\text{C}$	-	-	5	mA
Reverse Recovery Time	t_{rr}	$I_F = 0.5\text{A}$, $I_R = 1\text{A}$, $I_{RR} = 0.25\text{A}$	-	22	.35	ns

Notes:

1. The data tested by surface mounted on p.c.b. with 10 mm x 20 mm x 0.1 mm copper pad area
2. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 1\%$
3. The data tested by pulsed, pulse width $\leq 40\text{ms}$

Ratings and Characteristic Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

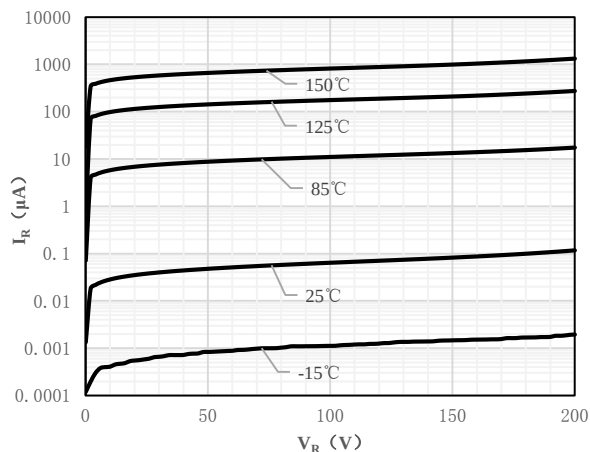


Fig 1 Typical Reverse Characteristic

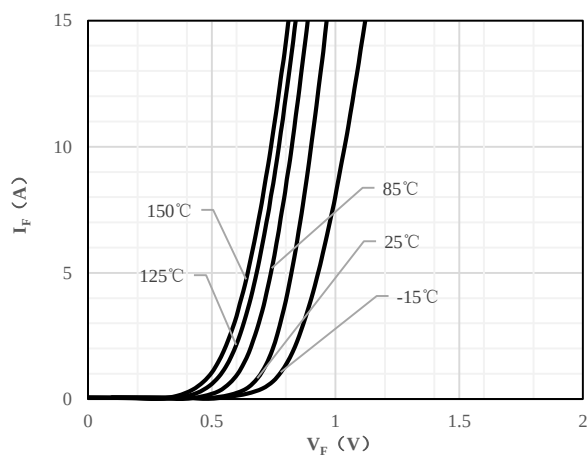


Fig 2 Typical Forward Characteristics

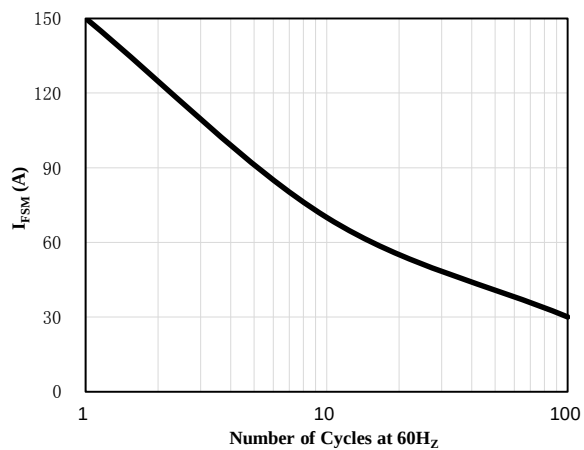


Fig 3 Surge Current Derating Curve

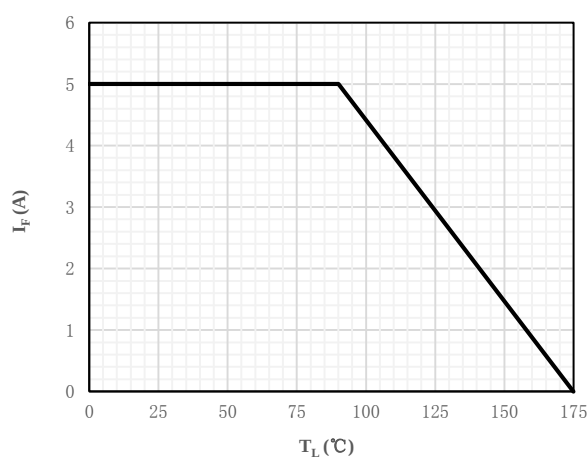


Fig 4 Forward Current Derating Curve

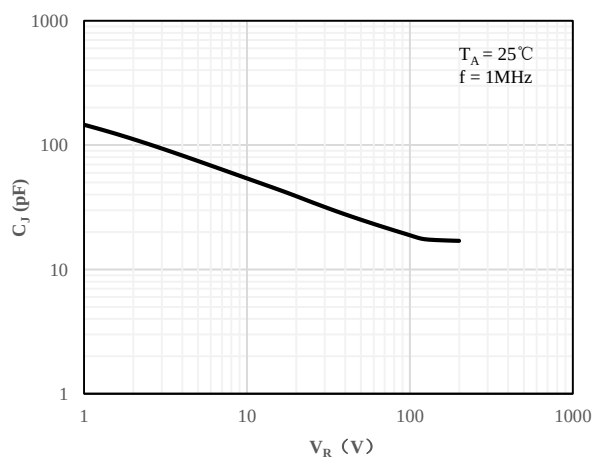
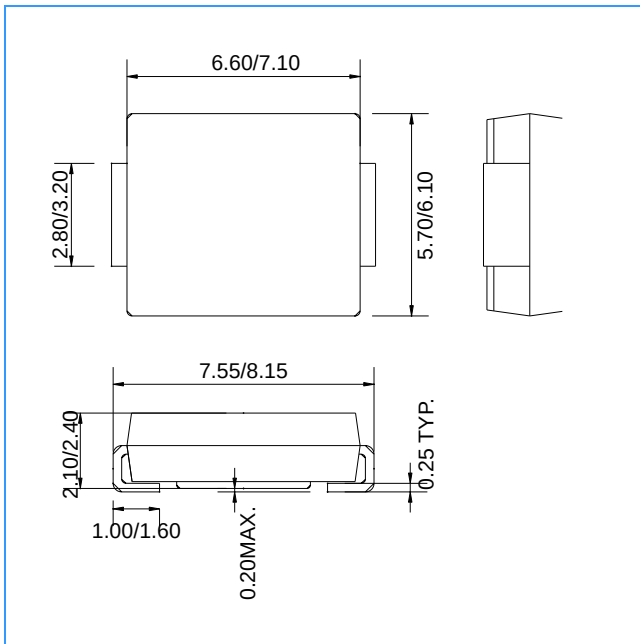


Fig 5 Capacitance vs. Reverse Voltage

Package Outline Dimensions (unit: mm)



Mounting Pad Layout (Unit: mm)

