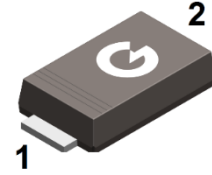


Features

- Metal silicon junction, majority carrier conduction
- High forward surge current capability
- Low forward voltage drop
- High temperature soldering guaranteed: 260°C @ 10s
- Robust guarding for over voltage protection
- RoHS compliant with Halogen-free
- Qualified to AEC-Q101 Standards

HF



SMAF

Mechanical Data

- Case: SMAF
- Molding compound: UL flammability classification rating 94-0
- Terminals: Tin-plated; solderability per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
TSS38F	SMAF	10000 pcs / Tape & Reel	SS38F
TSS39F	SMAF	10000 pcs / Tape & Reel	SS39F
TSS310F	SMAF	10000 pcs / Tape & Reel	SS310F

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	TSS38F	TSS39F	TSS310F	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	80	90	100	V
RMS Reverse Voltage	V _{RMS}	56	63	70	V
DC Blocking Voltage	V _{DC}	80	90	100	V
Average Forward Output Current	I _{F(AV)}	3.0			A
Peak Forward Surge Current, 8.3ms Single Half Sine wave	I _{FSM}	80			A

Thermal Characteristics

Parameter	Symbol	TSS38F	TSS39F	TSS310F	Unit
Thermal Resistance Junction-to-Air *1	R _{θJA}	78			°C/W
Thermal Resistance Junction-to-Case *1	R _{θJC}	20			°C/W
Thermal Resistance Junction-to-Lead *1	R _{θJL}	18			°C/W
Operating Junction Temperature Range	T _J	-55 ~ +150			°C
Storage Temperature Range	T _{STG}	-55 ~ +150			°C

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Reverse Breakdown Voltage	$V_{(BR)}$	$I_R = 100\mu\text{A}$, TSS38F	80	-	-	V
		$I_R = 100\mu\text{A}$, TSS39F	90	-	-	V
		$I_R = 100\mu\text{A}$, TSS310F	100	-	-	V
Forward Voltage *2	V_F	$I_F = 3\text{A}$	-	-	0.8	V
Maximum Peak Reverse Current *3	I_R	Rated V_R , $T_J = 25^\circ\text{C}$	-	-	10	μA
		Rated V_R , $T_J = 100^\circ\text{C}$	-	-	1	mA
Diode capacitance	C_J	$V_R = 4\text{V}$, $f = 1\text{MHz}$	-	92	-	pF

Notes:

1. The data tested by surface mounted on p.c.b. with 10 mm x 20 mm x 0.1 mm copper pad area
2. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 1\%$
3. The data tested by pulsed, pulse width $\leq 40\text{ms}$

Ratings and Characteristic Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

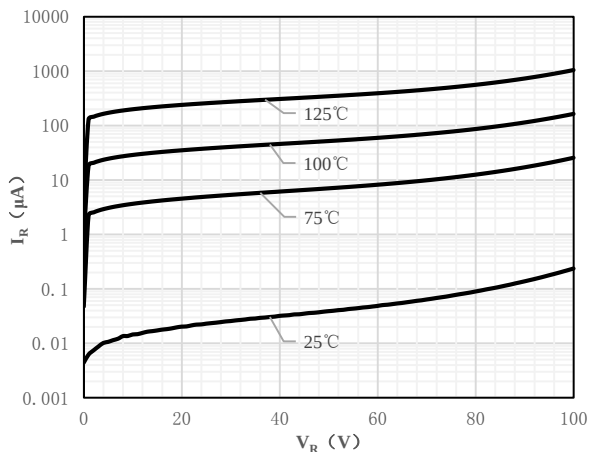


Fig 1 Typical Reverse Characteristic

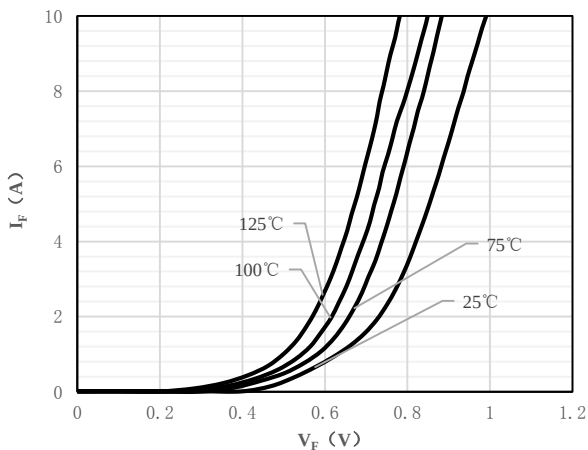


Fig 2 Typical Forward Characteristics

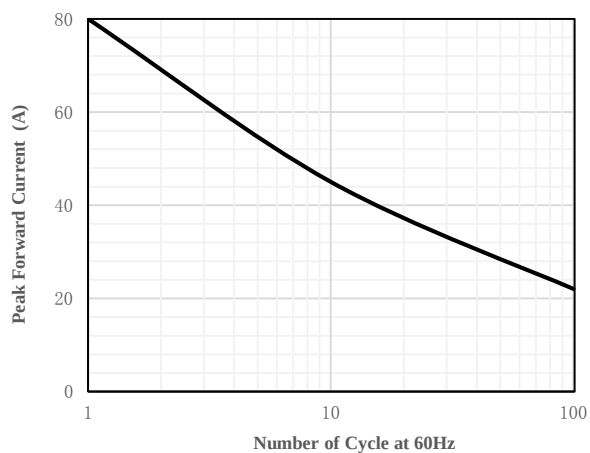


Fig 3 Surge Current Derating Curve

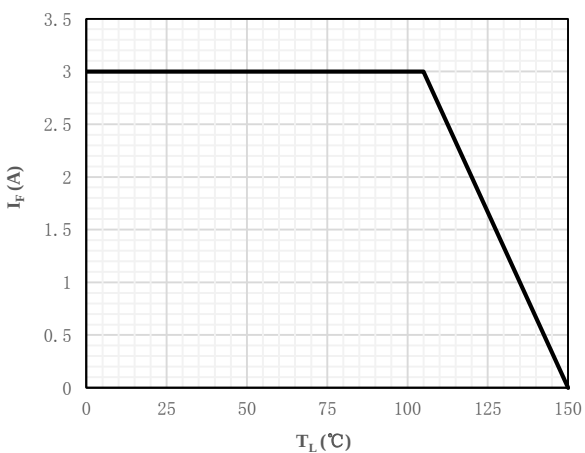


Fig 4 Forward Current Derating Curve

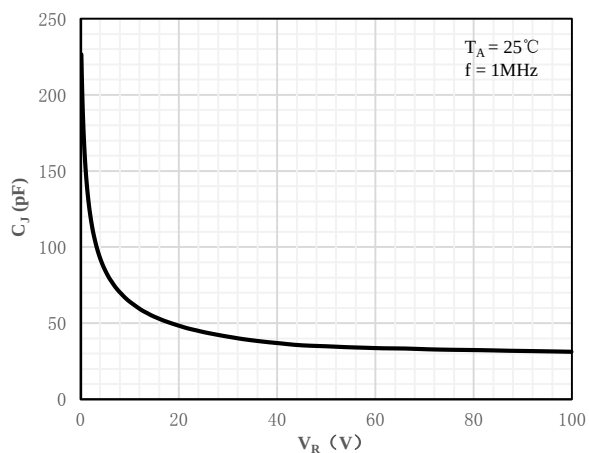
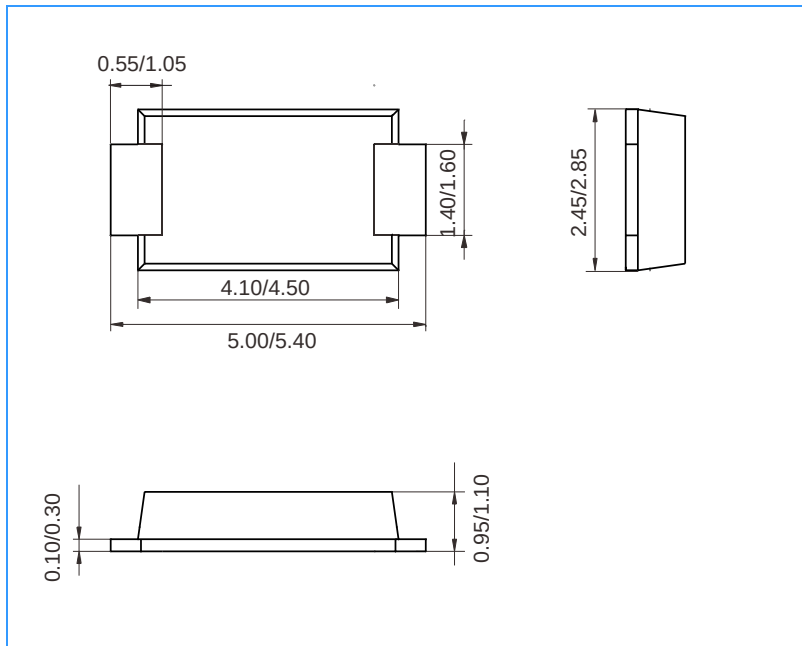


Fig 5 Capacitance vs. Reverse Voltage

Package Outline Dimensions (Unit: mm)



Mounting Pad Layout (Unit: mm)

