Dual PNP Silicon Epitaxial Planar Transistor

TMMDT3906

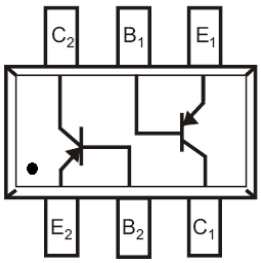
Features

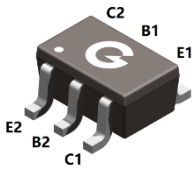
HF

- Epitaxial planar die construction
- Complimentary to TMMDT3904
- Ultra-small surface mount package
- RoHS compliant with Halogen-free
- Qualified to AEC-Q101 Standards

Mechanical Data

- Case: SOT-363
- Molding compound: UL flammability classification rating 94V-0
- Terminals: Tin-plated; solderability per MIL-STD-202, Method 208





SOT-363

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
TMMDT3906	SOT-363	3000 pcs / Tape & Reel	K3N

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Base Breakdown Voltage	V_{CBO}	-40	V
Collector-Emitter Breakdown Voltage	V_{CEO}	-40	V
Emitter-Base Breakdown Voltage	V_{EBO}	-5	V
Collector Current (Continuous)	I_C	-0.2	A
Collector Current (Peak)	I_{CM}	-0.2	A

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation (T_A = 25°C)	P_D	200	mW
Thermal Resistance Junction-to-Air	$R_{\theta JA}$	625	°C/W
Thermal Resistance Junction-to-Air *1	$R_{\theta JA}$	460	°C/W
Thermal Resistance Junction-to-Case *1	$R_{\theta JC}$	320	°C/W
Thermal Resistance Junction-to-Lead *1	$R_{\theta JL}$	340	°C/W
Ambient Temperature	T_A	-55 ~ +150	°C
Operating junction Temperature	T_J	-55 ~ +150	°C
Storage Temperature Range	T_{STG}	-55 ~ +150	°C

Note 1: The data tested by surface mounted on a 15mm * 15mm * 1mm FR4-epoxy P.C.B

Electrical Characteristics (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	I_C = -10μA, I_E = 0	-40	-	-	V
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	I_C = -1mA, I_B = 0	-40	-	-	V
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	I_E = -10μA, I_C = 0	-5	-	-	V
Collector Cut-off Current	I_{CEX}	V_{CE} = -30V, $V_{EB(OFF)}$ = -3V	-	-	-50	nA
Base Cut-off Current	I_{BL}	V_{CE} = -30V, $V_{EB(OFF)}$ = -3V	-	-	-50	nA
DC Current Gain	h_{FE}	V_{CE} = -1V, I_C = -0.1mA	60	-	-	-
		V_{CE} = -1V, I_C = -1mA	80	-	-	-
		V_{CE} = -1V, I_C = -10mA	100	-	300	-
		V_{CE} = -1V, I_C = -50mA	60	-	-	-
		V_{CE} = -1V, I_C = -100mA	30	-	-	-
Collector-emitter Saturation Voltage	$V_{CE(sat)}$	I_C = -10mA, I_B = -1mA	-	-	-0.25	V
		I_C = -50mA, I_B = -5mA	-	-	-0.4	V
Base-emitter Saturation Voltage	$V_{BE(sat)}$	I_C = -10mA, I_B = -1mA	-0.65	-	-0.85	V
		I_C = -50mA, I_B = -5mA	-	-	-0.95	V
Transition Frequency	f_T	I_C = -1mA, V_{CE} = -10V, f = 1kHz	250	-	-	MHz
Collector Output Capacitance	C_{OBO}	V_{CB} = -5V, I_E = 0, f = 1MHZ	-	-	4.5	pF
Input Capacitance	C_{IBO}	V_{EB} = -0.5V, I_C = 0, f = 1MHz	-	-	10	pF
Noise Figure	N_F	V_{CE} = -20V, I_C = -100μA f = 100MHz	-	-	4	dB
Delay Time	t_d	V_{CC} = -3V, I_C = -10mA $V_{BE(OFF)}$ = -0.5V, I_{B1} = I_{B2} = 1mA	-	-	35	ns
Rise Time	t_r		-	-	35	ns
Storang Time	t_s	V_{CC} = -3V, I_C = -10mA I_{B1} = I_{B2} = 1mA	-	-	225	ns
Fall Time	t_f		-	-	75	ns

Ratings and Characteristics Curves (@ T_A = 25°C unless otherwise specified)

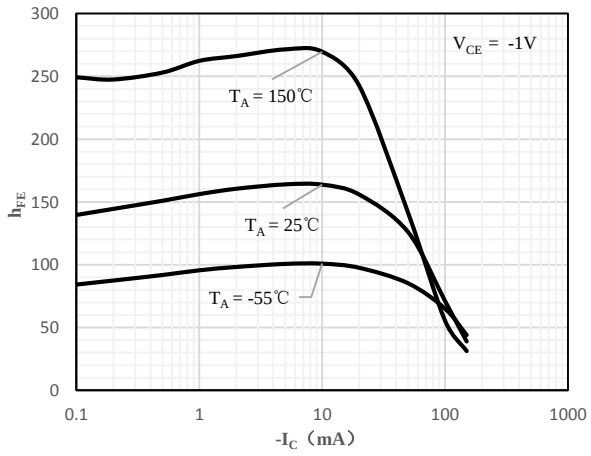


Fig 1 h_{FE} vs. I_C

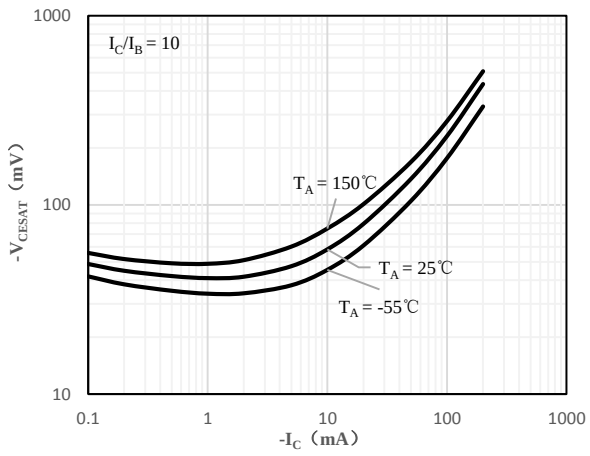


Fig 2 $V_{CE(sat)}$ vs. I_C

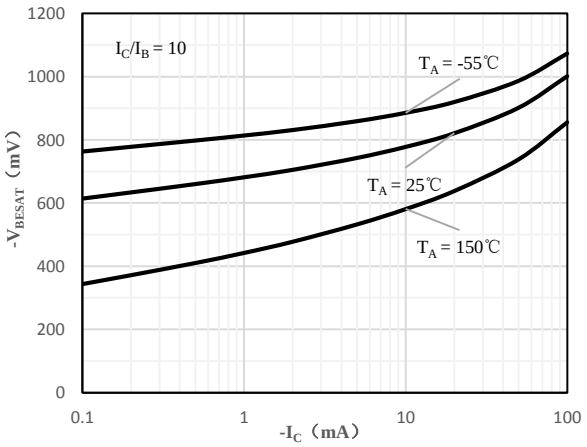


Fig 3 $V_{BE(sat)}$ vs. I_C

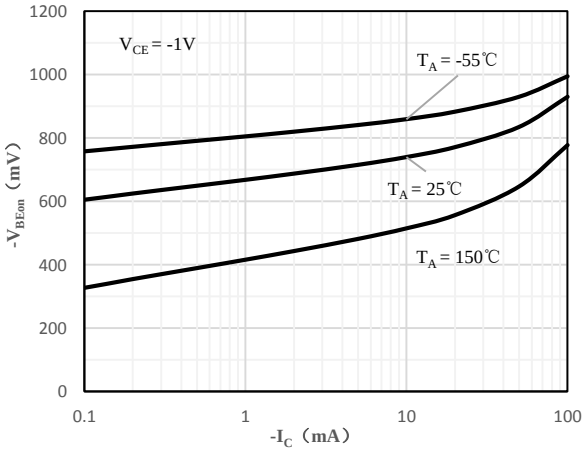


Fig 4 $V_{BE(ON)}$ vs. I_C

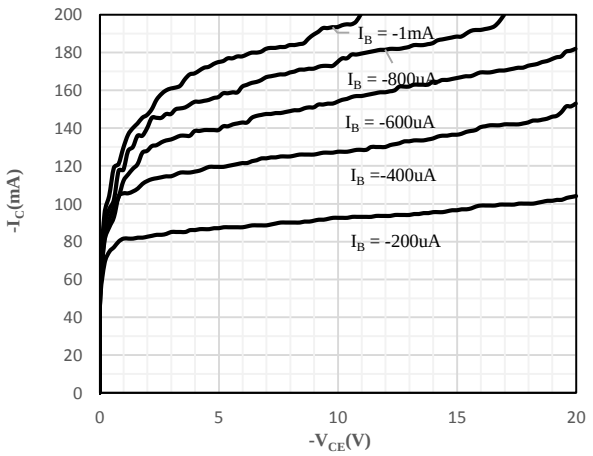


Fig 5 I_C vs. V_{CE}

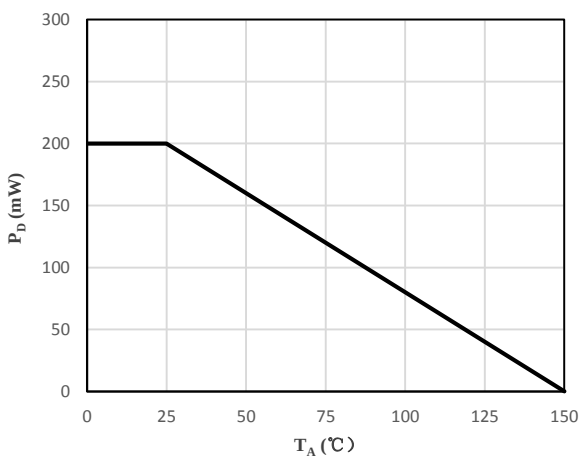


Fig 6 P_D vs. T_A

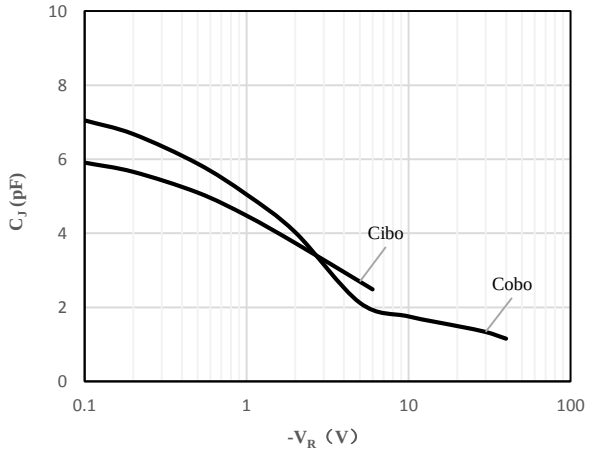
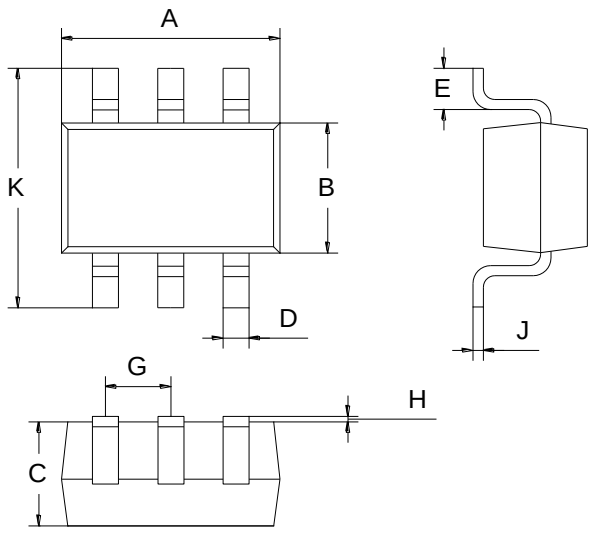


Fig 7 C_J vs. V_R

Package Outline Dimensions (Unit: mm)



SOT-363		
Dimension	**Min.**	**Max.**
A	2.00	2.20
B	1.15	1.35
C	0.85	1.05
D	0.15	0.35
E	0.25	0.40
G	0.60	0.70
H	0.02	0.10
J	0.05	0.15
K	2.20	2.40

Mounting Pad Layout (Unit: mm)

SOT-363

