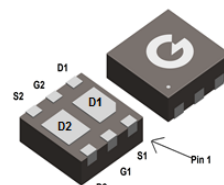
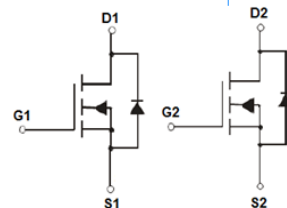


Features

- Advanced SGT technology
- Low $R_{DS(ON)} \times Q_G$ FOM
- Extremely low switching loss
- Excellent stability and uniformity
- RoHS compliant with Halogen-free
- Qualified to AEC-Q101 Standards

HF



DFN2020-6LC

Mechanical Data

- Case: DFN2020-6LC
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
TGBLNAA02DF2	DFN2020-6LC	3000 pcs / Tape & Reel	NAA02

Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DS}	100	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$)	I_D	4	A
Continuous Drain Current ($T_A = 25^\circ\text{C}$) ^{*1}		2.5	A
Continuous Drain Current ($T_A = 70^\circ\text{C}$) ^{*1}		2	A
Pulsed Drain Current ($t_p = 10\mu\text{s}$, $T_A = 25^\circ\text{C}$)	I_{DM}	25	A
Single Pulse Avalanche Energy ^{*3}	E_{AS}	0.1	mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	4.2	W
Power Dissipation ($T_A = 25^\circ\text{C}$) ^{*1}		1.5	W
Operating Junction Temperature Range	T_J	$-55 \sim +150$	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	$-55 \sim +150$	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	-	30	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Air ^{*1}	$R_{\theta JA}$	-	-	83	$^\circ\text{C/W}$

Electrical Characteristics (@ $T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 80V, V _{GS} = 0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance *2	V _{GS} = 10V, I _D = 3A	-	105	130	mΩ
		V _{GS} = 4.5V, I _D = 2A	-	135	165	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	1	1.5	2.5	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	6	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V V _{DS} = 50V f = 1MHz	-	143	-	pF
C _{OSS}	Output Capacitance		-	50	-	
C _{RSS}	Reverse Transfer Capacitance		-	2.8	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time *4	V _{DD} = 50V V _{GS} = 10V R _G = 2Ω I _D = 3A	-	13.2	-	ns
t _r	Turn-on Rise Time *4		-	2.2	-	
t _{d(OFF)}	Turn-Off Delay Time *4		-	11	-	
t _f	Turn-Off Fall Time *4		-	1.1	-	
Q _G	Total Gate-Charge (V _{GS} = 4.5V)	V _{DD} = 50V V _{GS} = 10V I _D = 3A	-	3	-	nC
	Total Gate-Charge (V _{GS} = 10V)		-	6	-	
Q _{GS}	Gate to Source Charge		-	1.4	-	
Q _{GD}	Gate to Drain (Miller) Charge		-	0.8	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage *2	I _{SD} = 3A, V _{GS} = 0V	-	0.9	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _F = 3A	-	30	-	ns
Q _{rr}	Reverse Recovery Charge	di _F /d _t = 100A/μs	-	24	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The E_{AS} data shows Max. rating. The test condition is $V_{DD} = 50V, V_{GS} = 10V, L = 0.5mH$
- Guaranteed by design, not subject to production

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

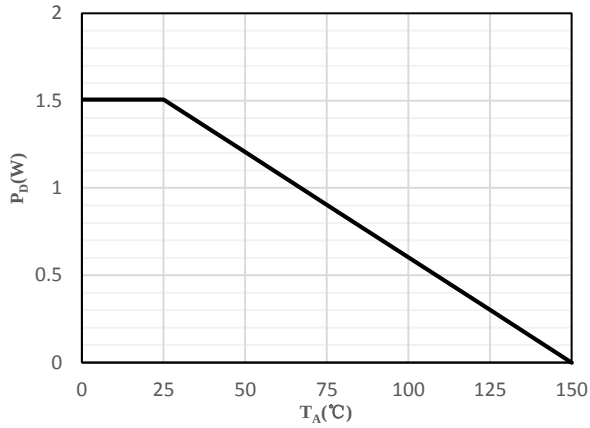


Fig 1 Power Dissipation

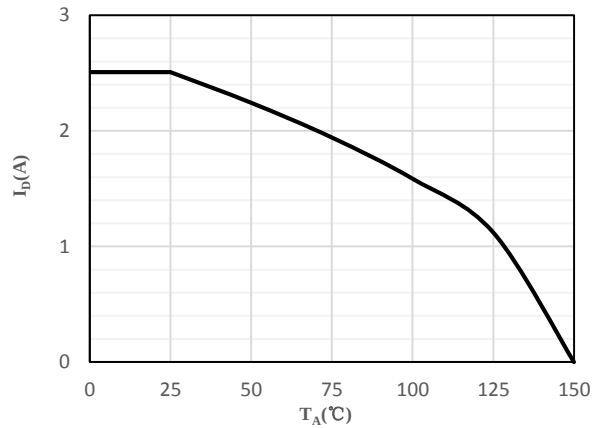


Fig 2 Drain Current

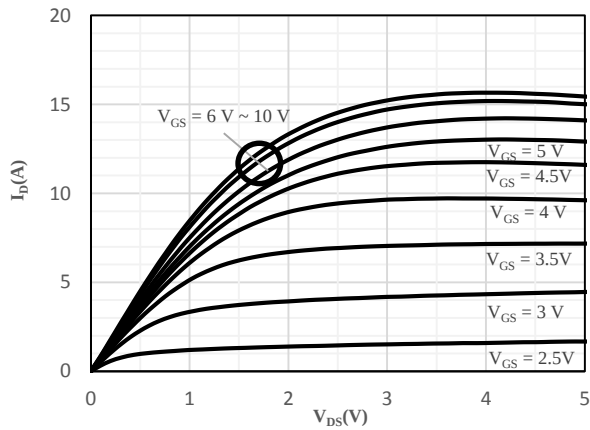


Fig 3 Typical Output Characteristics

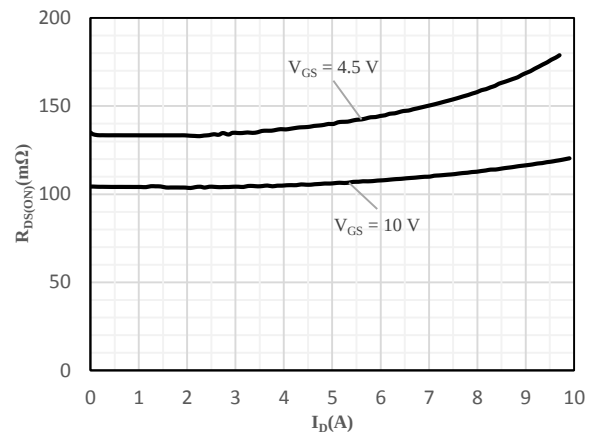


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

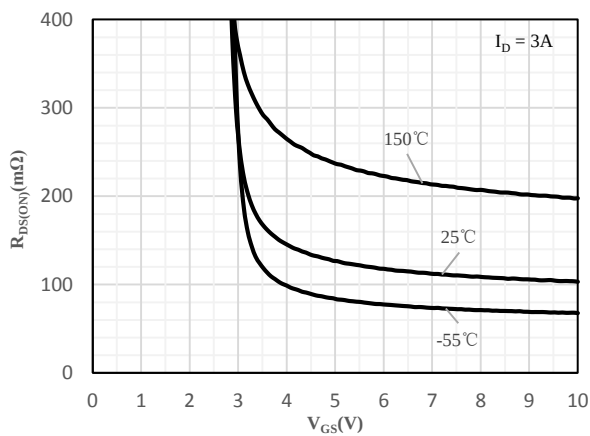


Fig 5 On-Resistance vs. Gate-Source Voltage

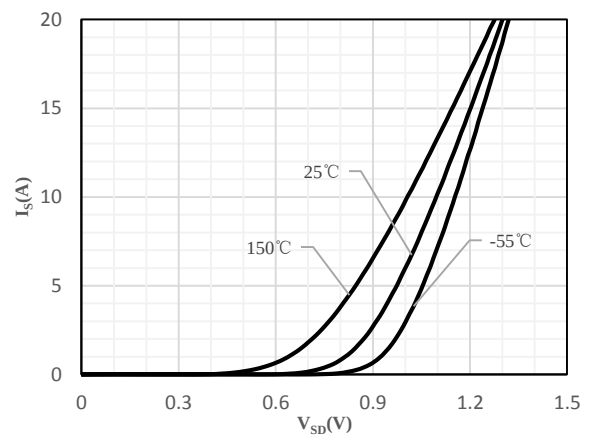


Fig 6 Body-Diode Characteristics

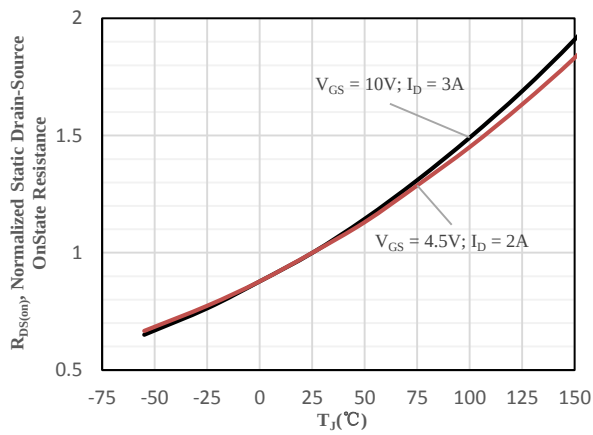


Fig 7 Normalized On-Resistance vs. Junction Temperature

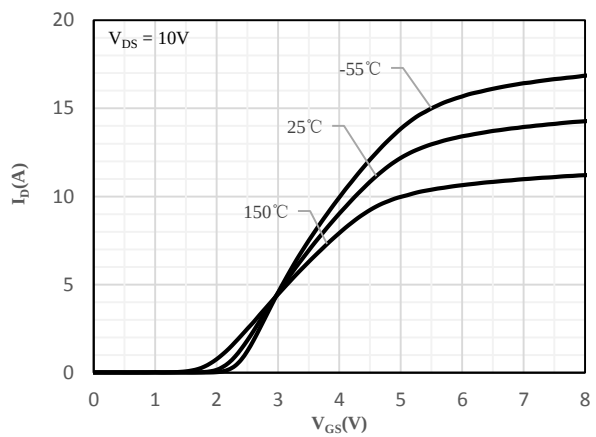


Fig 8 Transfer Characteristics

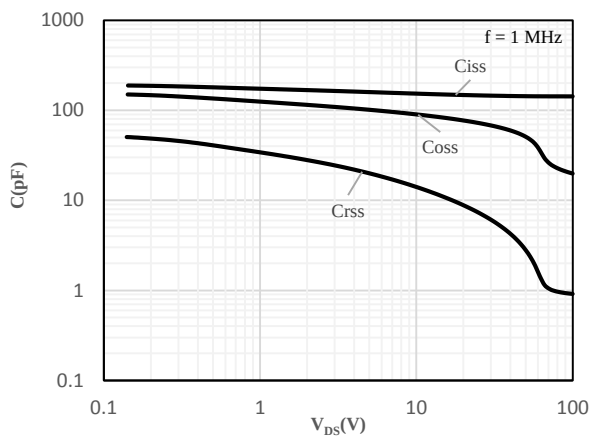


Fig 9 Capacitance Characteristics

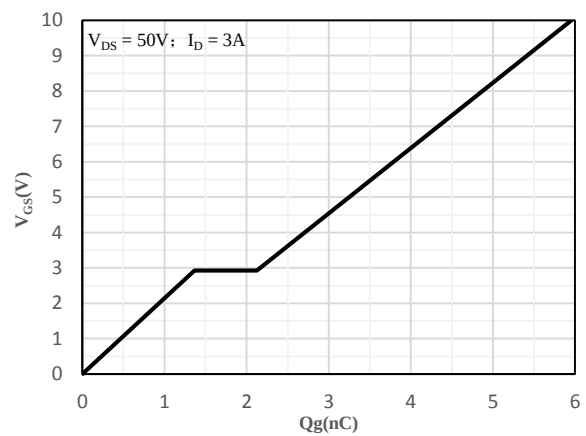


Fig 10 Gate-Charge Characteristics

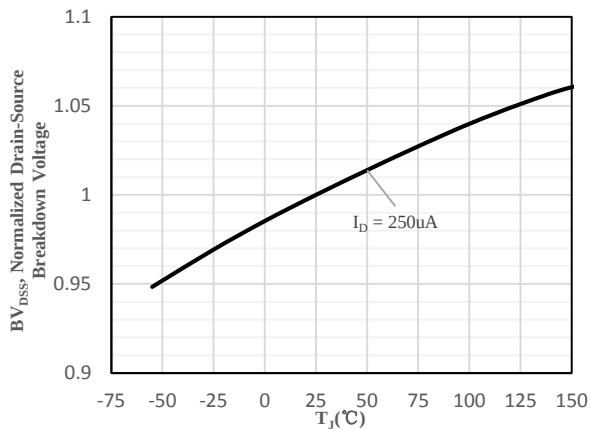


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

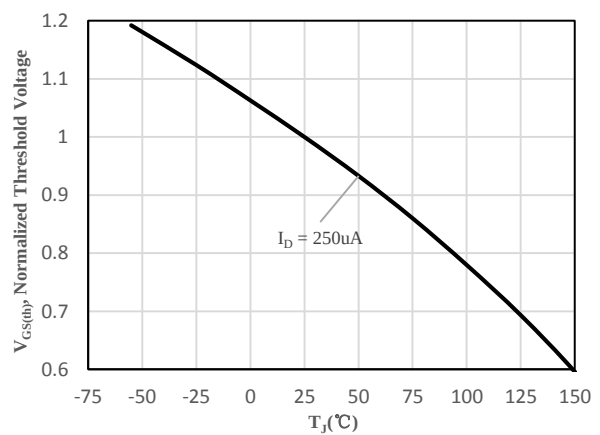
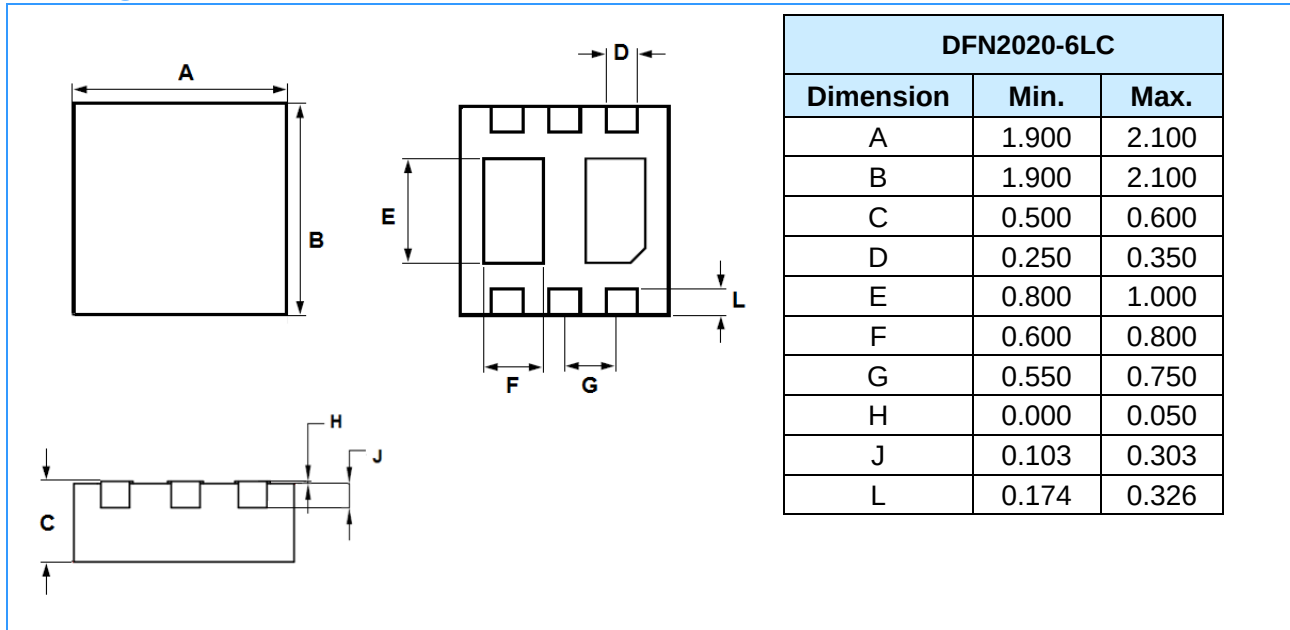


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

Package Outline Dimensions (Unit: mm)



Mounting Pad Layout (Unit: mm)

