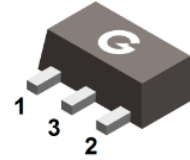
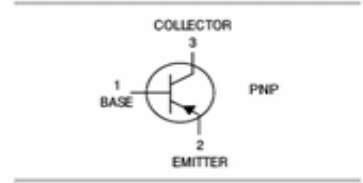


Features

- Epitaxial planar die construction
- High current
- Low voltage
- Complimentary to PXT4401
- RoHS compliant with Halogen-free

HF



SOT-89

Mechanical Data

- Case: SOT-89
- Molding compound: UL flammability classification rating 94V-0
- Terminals: Tin-plated; solderability per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
PXT4403	SOT-89	1000 pcs / Tape & Reel	2T

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Base Breakdown Voltage	V _{CBO}	-40	V
Collector-Emitter Breakdown Voltage	V _{CEO}	-40	V
Emitter-Base Breakdown Voltage	V _{EBO}	-5	V
Collector Current (Continuous)	I _C	-0.6	A
Collector Current (Peak)	I _{CM}	-0.8	A

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation (T _A = 25°C) *1	P _D	1.25	W
Thermal Resistance Junction-to-Air *1	R _{θJA}	100	°C/W
Thermal Resistance Junction-to-Case *1	R _{θJC}	45	°C/W
Thermal Resistance Junction-to-Lead *1	R _{θJL}	10	°C/W
Ambient Temperature	T _A	-55 ~ +150	°C
Operating junction Temperature	T _J	-65 ~ +150	°C
Storage Temperature Range	T _{STG}	-65 ~ +150	°C

Note 1: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C = -10\mu\text{A}, I_E = 0$	-40	-	-	V
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C = -10\text{mA}, I_B = 0$	-40	-	-	V
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E = -10\mu\text{A}, I_C = 0$	-5	-	-	V
Collector Cut-off Current	I_{CBO}	$V_{CB} = -40\text{V}, I_E = 0$	-	-	-50	nA
Emitter Cut-off Current	I_{EBO}	$V_{EB} = -5\text{V}, I_C = 0$	-	-	-50	nA
DC Current Gain	h_{FE}	$V_{CE} = -1\text{V}, I_C = -0.1\text{mA}$	30	-	-	-
		$V_{CE} = -1\text{V}, I_C = -1\text{mA}$	60	-	-	-
		$V_{CE} = -1\text{V}, I_C = -10\text{mA}$	100	-	-	-
		$V_{CE} = -2\text{V}, I_C = -150\text{mA}$	100	-	300	-
		$V_{CE} = -2\text{V}, I_C = -500\text{mA}$	20	-	-	-
Collector-emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = -150\text{mA}, I_B = -15\text{mA}$	-	-	-0.4	V
		$I_C = -500\text{mA}, I_B = -50\text{mA}$	-	-	-0.75	V
Base-emitter Saturation Voltage	$V_{BE(sat)}$	$I_C = -150\text{mA}, I_B = -15\text{mA}$	-	-	-0.95	V
		$I_C = -500\text{mA}, I_B = -50\text{mA}$	-	-	-1.3	V
Transition Frequency	f_T	$I_C = -20\text{mA}, V_{CE} = -10\text{V}$ $f = 100\text{MHz}$	200	-	-	MHz
Collector Output Capacitance	C_{OBO}	$V_{CB} = -10\text{V}, I_E = 0, f = 1\text{MHz}$	-	-	8	pF
Input Capacitance	C_{IBO}	$V_{EB} = -0.5\text{V}, I_C = 0, f = 1\text{MHz}$	-	-	35	pF
Delay Time	t_d	$I_C = -150\text{mA}$ $I_{B(on)} = -15\text{mA}$ $I_{B(off)} = -15\text{mA}$	-	-	15	ns
Rise Time	t_r		-	-	30	ns
Storage Time	t_s		-	-	300	ns
Fall Time	t_f		-	-	65	ns

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

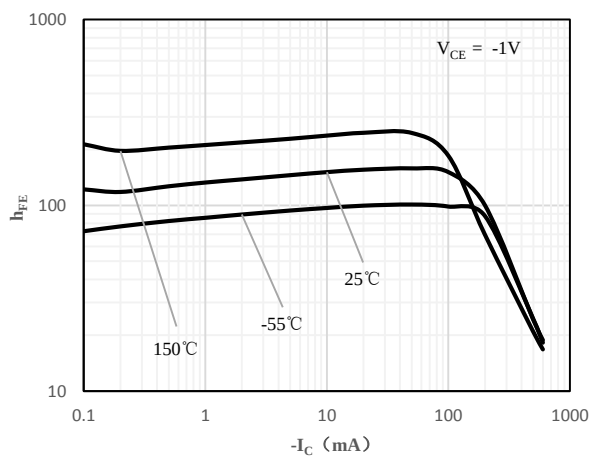


Fig 1 h_{FE} vs. I_C

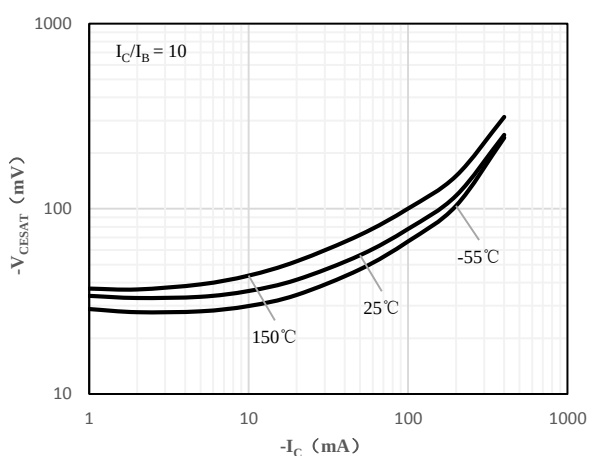


Fig 2 $V_{CE(sat)}$ vs. I_C

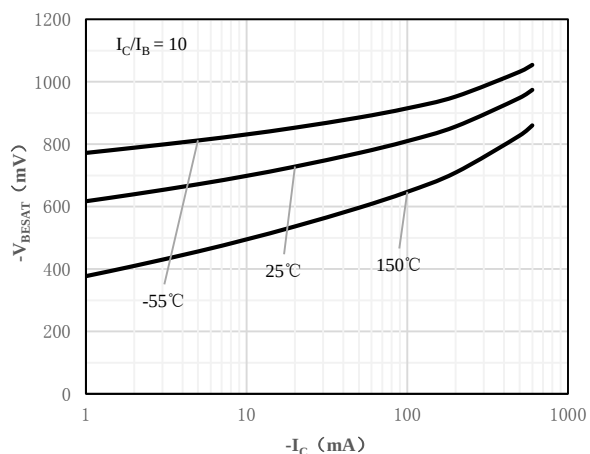


Fig 3 $V_{BE(sat)}$ vs. I_C

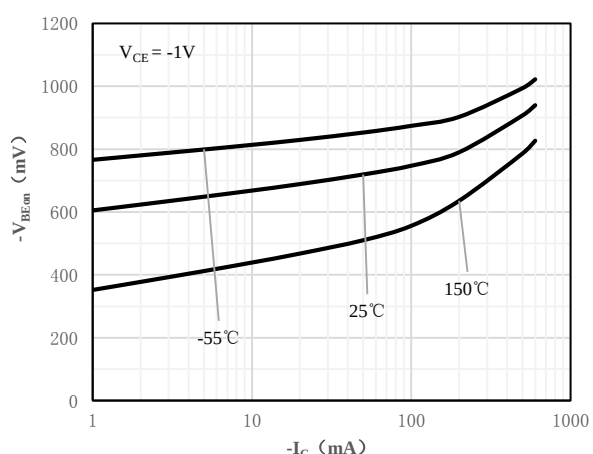


Fig 4 $V_{BE(on)}$ vs. I_C

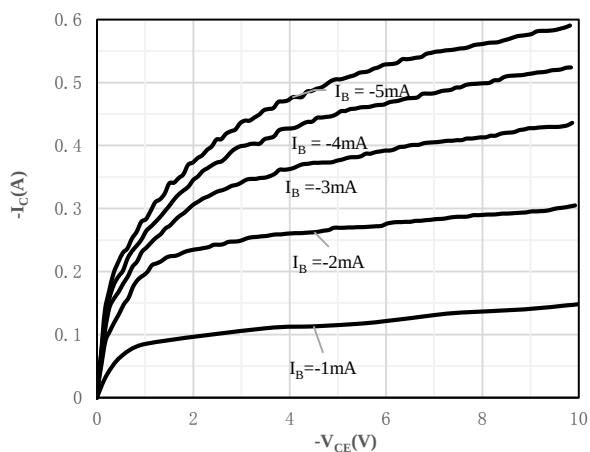


Fig 5 I_C vs. V_{CE}

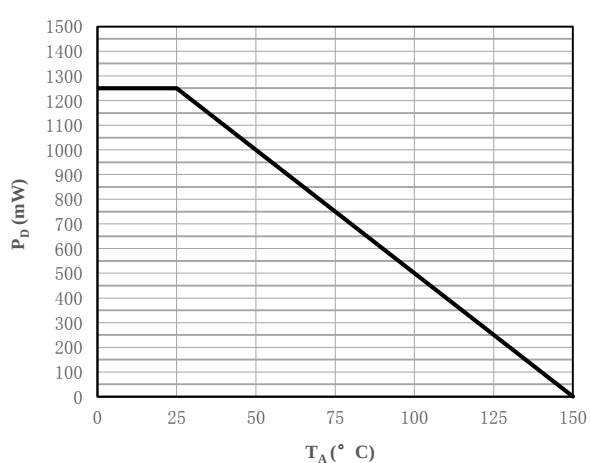


Fig 6 P_D vs. T_A

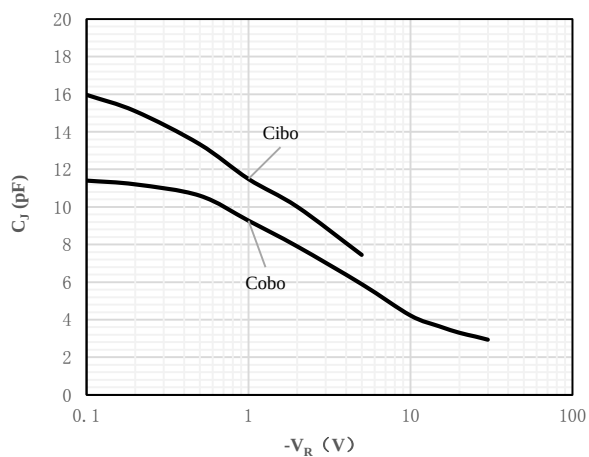
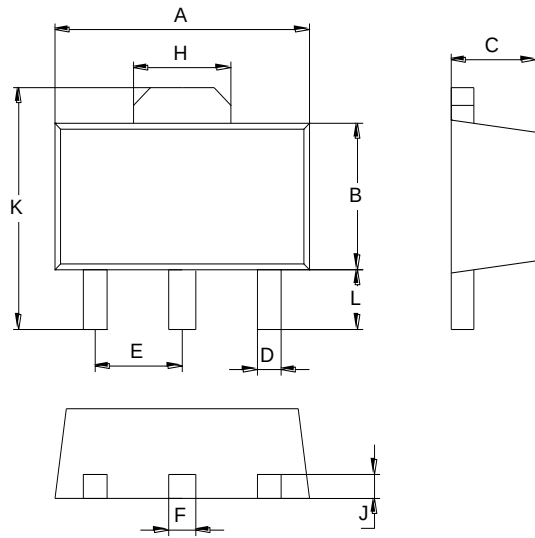


Fig 7 C_J vs. V_R

Package Outline Dimensions (Unit: mm)



SOT-89		
Dimension	Min.	Max.
A	4.30	4.70
B	2.25	2.65
C	1.30	1.70
D	0.30	0.50
E	1.40	1.60
F	0.38	0.58
H	1.60	1.80
J	0.30	0.50
L	0.90	1.10
K	3.95	4.35

Mounting Pad Layout (Unit: mm)

SOT-89

