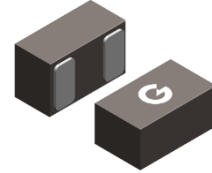


Features

- ESD / transient protection of high speed data lines
 - IEC 61000-4-2 (ESD): $\pm 25\text{kV}$ (air), $\pm 25\text{kV}$ (contact)
- Working voltage: $V_{\text{RWM}} = 5.5\text{V}$
- Low leakage current
- Low clamping voltage
- RoHS compliant with Halogen-free

HF



DFN1006-2

Mechanical Data

- Case: DFN1006-2
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
GESD5V5B2L	DFN1006-2	10000 pcs / Tape & Reel	N

Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
IEC 61000-4-2; ESD (Air)	$V_{\text{ESD-A}}$	± 25	kV
IEC 61000-4-2; ESD (Contact)	$V_{\text{ESD-C}}$	± 25	kV
Peak Pulse Power ($t_p = 8/20\mu\text{s}$)	P_{PP}	84	W
Peak Pulse Current ($t_p = 8/20\mu\text{s}$)	I_{PP}	7	A

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation	P_D	0.25	W
Thermal Resistance Junction-to-Air	$R_{\theta\text{JA}}$	400	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Lead	$R_{\theta\text{JL}}$	204	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Case	$R_{\theta\text{JC}}$	240	$^\circ\text{C/W}$
Junction Temperature Range	T_J	$-55 \sim +125$	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	$-55 \sim +150$	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = 25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Reverse Stand-off Voltage	V_{RWM}		-	-	5.5	V
Reverse Breakdown Voltage	$V_{(BR)}$	$I_T = 1\text{mA}$	6	-	8	V
Reverse Leakage Current	I_R	$V_{RWM} = 5.5\text{V}$	-	-	100	nA
Clamping Voltage	V_C	$I_{PP} = 1\text{A}$, $t_p = 8/20\mu\text{s}$	-	-	8.5	V
		$I_{PP} = 7\text{A}$, $t_p = 8/20\mu\text{s}$	-	-	12.0	V
Junction Capacitance	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$	-	16	18	pF

Ratings and Characteristic Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

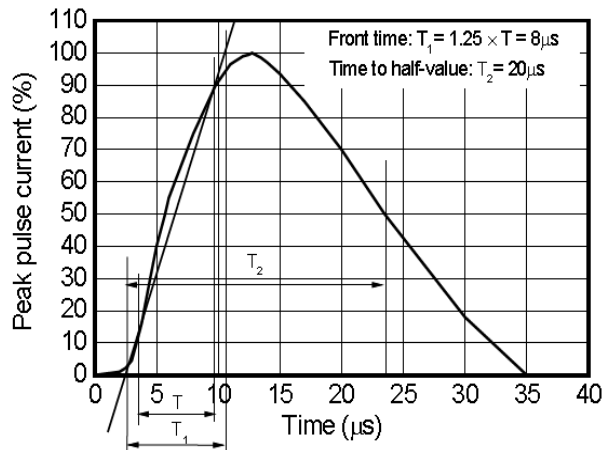


Fig 1 8/20 μs waveform per IEC61000-4-5

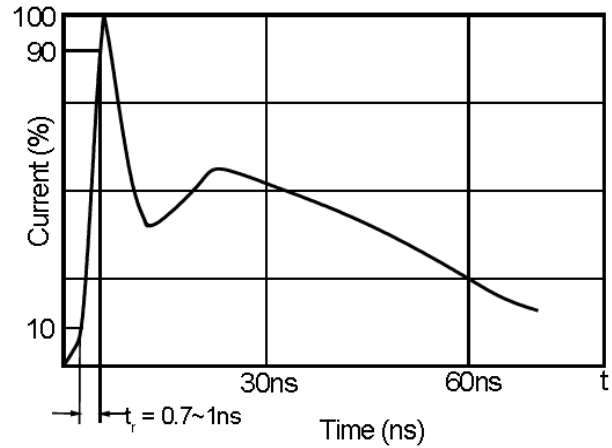


Fig 2 ESD pulse waveform according to IEC61000-4-2

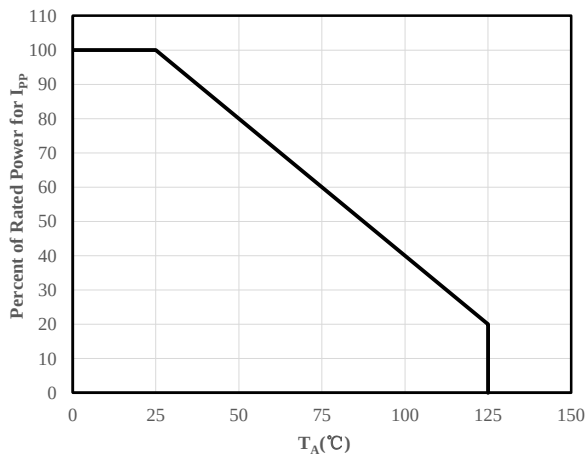


Fig 3 Power Derating Curve

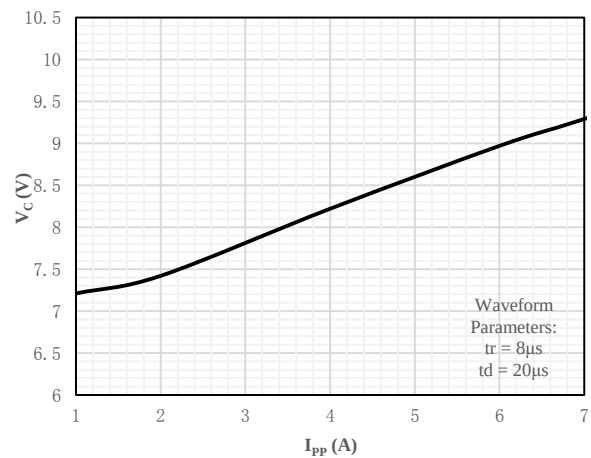


Fig 4 Clamping Voltage vs. Peak Pulse Current

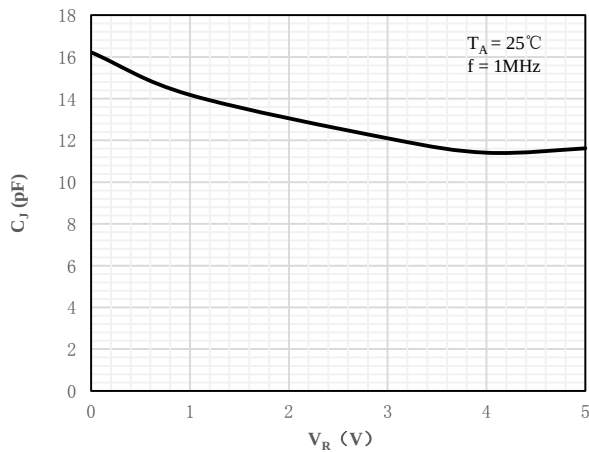
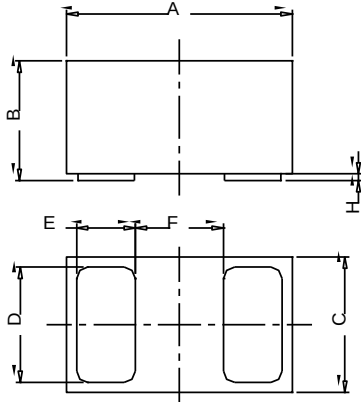


Fig 5 Capacitance vs. Reverse Voltage

Package Outline Dimensions (Unit: mm)



DFN1006-2			
Dimension	Min.	Typ.	Max.
A	0.95	1.00	1.075
B	0.45	0.50	0.55
C	0.55	0.60	0.675
D	0.45	0.50	0.55
E	0.20	0.25	0.30
F	-	0.40	-
H	0	0.03	0.05

Package Outline Dimensions (Unit: mm)

DFN1006-2

