

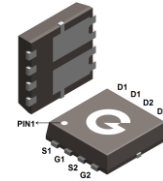
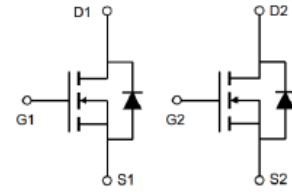
Features

- Advanced trench technology
- Low gate charge
- Low on-resistance
- Fast switching speed
- RoHS compliant with Halogen-free

Mechanical Data

- Case: PDFN3×3-8LC
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

HF



PDFN3×3-8LC

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
GBLN3301-3DL8	PDFN3×3-8LC	5000 pcs / Tape & Reel	120N03D

Maximum Ratings (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DS}	30	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$)	I_D	35	A
Continuous Drain Current ($T_C = 100^\circ\text{C}$)		22	A
Continuous Drain Current ($T_A = 25^\circ\text{C}$) ^{*1}		9.5	A
Continuous Drain Current ($T_A = 100^\circ\text{C}$) ^{*1}		6	A
Pulsed Drain Current ($t_p \leq 10\mu\text{s}$, $T_C = 25^\circ\text{C}$)	I_{DM}	140	A
Single Pulse Avalanche Energy ^{*3}	E_{AS}	16	mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	20	W
Power Dissipation ($T_A = 25^\circ\text{C}$) ^{*1}		1.8	W
Operating Junction Temperature Range	T_J	$-55 \sim +150$	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	$-55 \sim +150$	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	4.2	6.25	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Air ^{*1}	$R_{\theta JA}$	-	65	70	$^\circ\text{C/W}$

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 30V, V _{GS} = 0V	-	-	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance ^{*2}	V _{GS} = 10V,I _D = 20A	-	11	13	mΩ
		V _{GS} = 4.5V,I _D = 10A	-	16	18	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	1	1.5	2.5	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	4	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V V _{DS} = 15V f = 1.0MHz	-	710	-	pF
C _{OSS}	Output Capacitance		-	106	-	
C _{RSS}	Reverse Transfer Capacitance		-	83	-	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time ^{*4}	V _{DD} = 15V V _{GS} = 10V I _D = 20A R _G = 3Ω	-	3	-	ns
t _r	Turn-on Rise Time ^{*4}		-	2	-	
t _{d(OFF)}	Turn-Off Delay Time ^{*4}		-	13	-	
t _f	Turn-Off Fall Time ^{*4}		-	6	-	
Q _G	Total Gate-Charge	V _{DD} = 15V V _{GS} = 10V I _D = 20A	-	13.5	-	nC
Q _{GS}	Gate to Source Charge		-	2.5	-	
Q _{GD}	Gate to Drain (Miller) Charge		-	3.3	-	
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage ^{*2}	I _{SD} = 20A, V _{GS} = 0V	-	0.9	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} = 8A, V _{GS} = 0V	-	110	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt = 100A/μs	-	53	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The E_{AS} data shows Max. rating. The test condition is $V_{DD} = 15V, V_{GS} = 10V, L = 0.1mH$
- Guaranteed by design, not subject to production

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

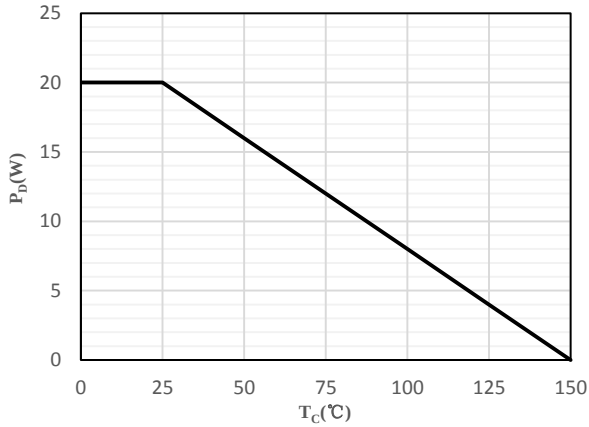


Fig 1 Power Dissipation

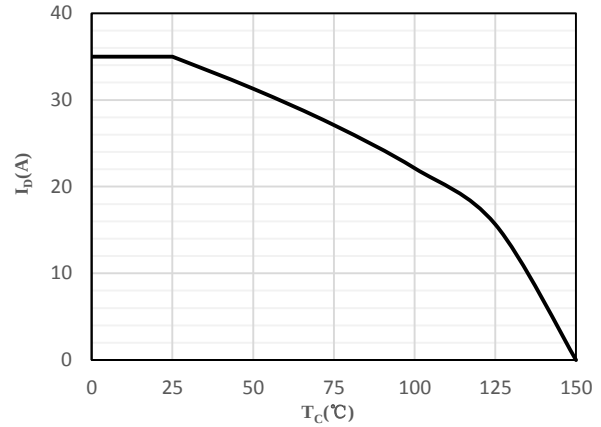


Fig 2 Drain Current

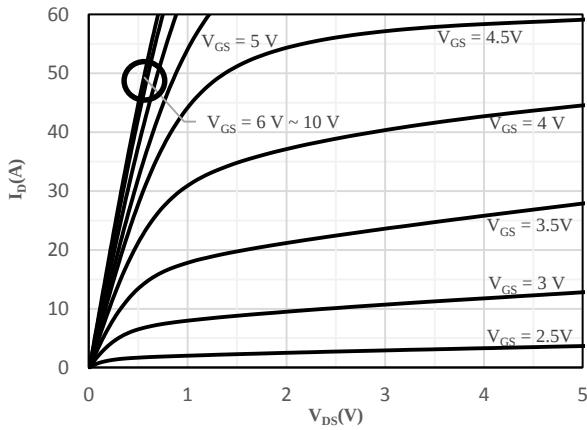


Fig 3 Typical Output Characteristics

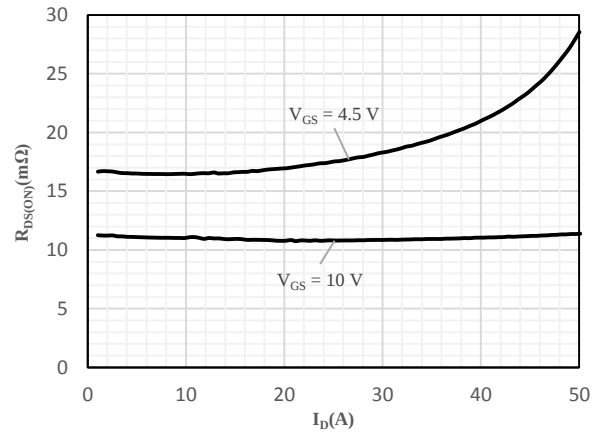


Fig 4 On-Resistance vs. Drain Current
and Gate Voltage

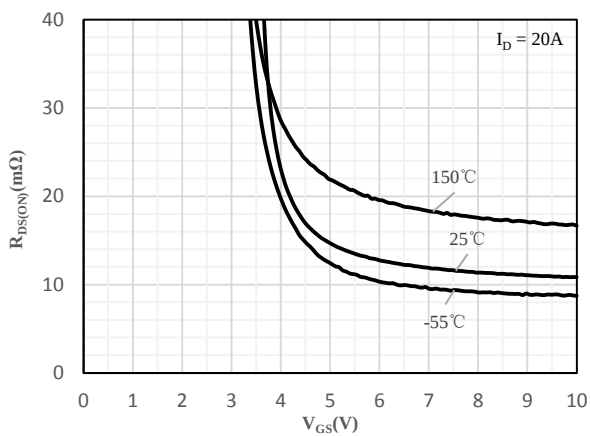


Fig 5 On-Resistance vs. Gate-Source Voltage

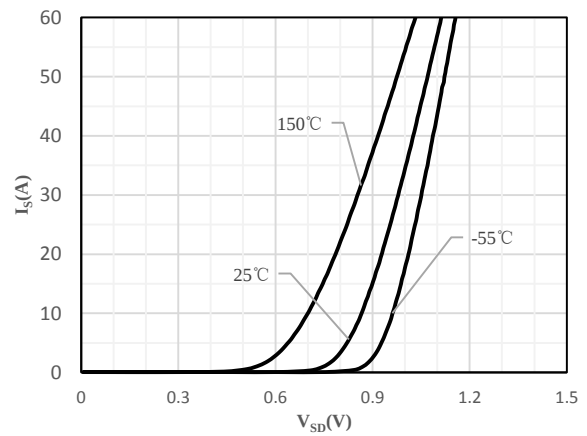


Fig 6 Body-Diode Characteristics

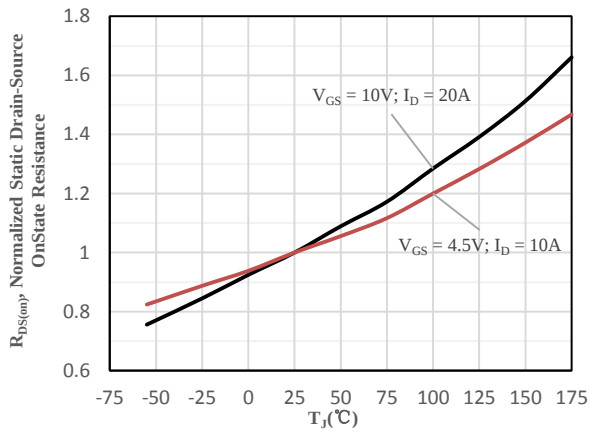


Fig 7 Normalized On-Resistance vs. Junction Temperature

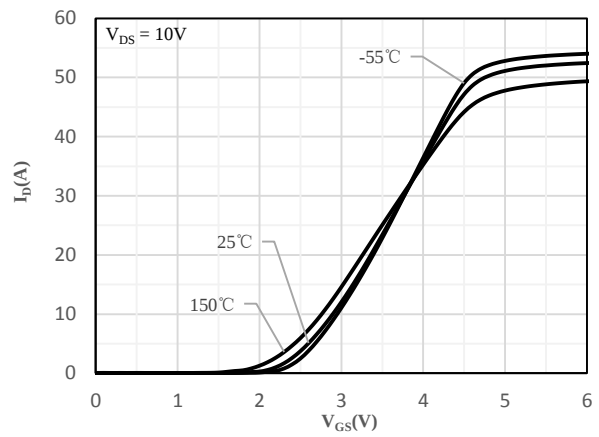


Fig 8 Transfer Characteristics

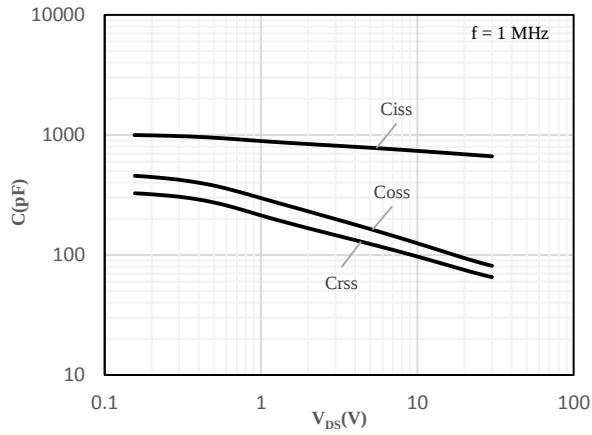


Fig 9 Capacitance Characteristics

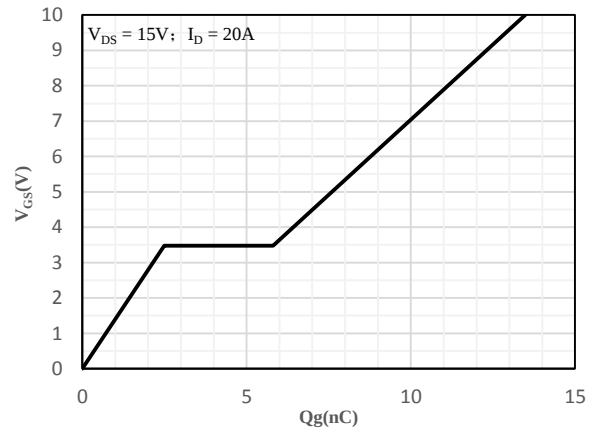


Fig 10 Gate-Charge Characteristics

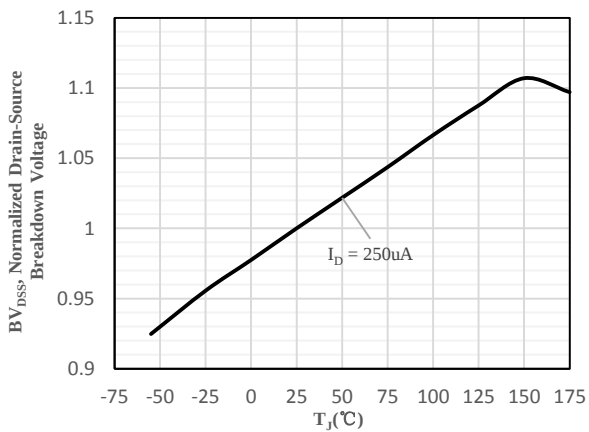


Fig 11 Normalized Breakdown Voltage vs. Junction Temperature

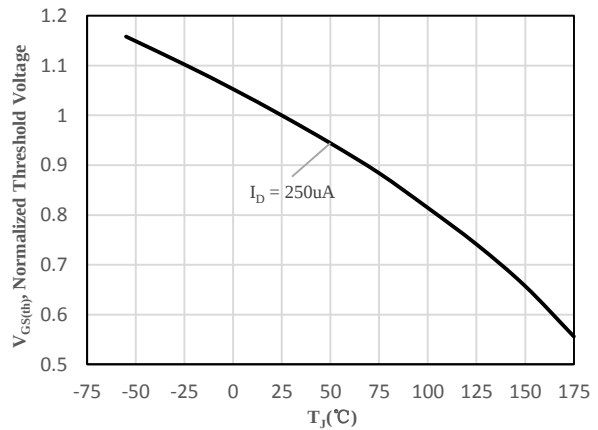


Fig 12 Normalized $V_{GS(th)}$ vs. Junction Temperature

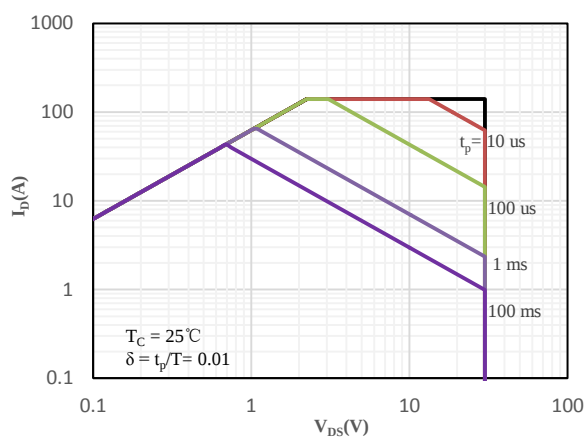


Fig 13 Safe Operating Area

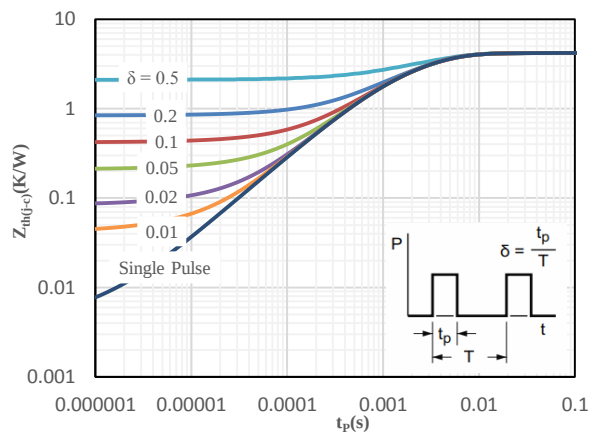
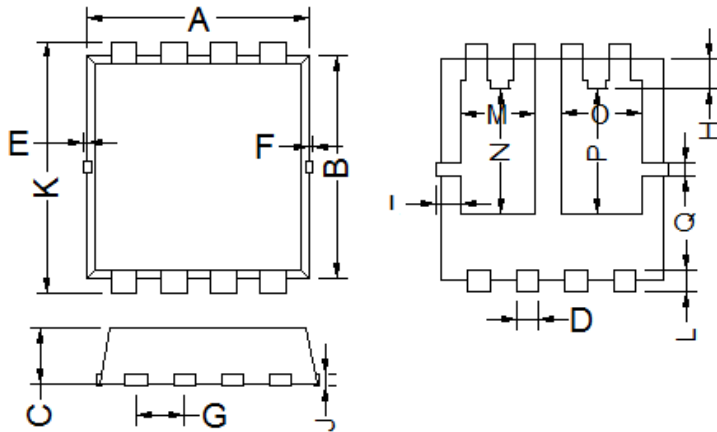


Fig 14 Maximum transient thermal impedance

Package Outline Dimensions (Unit: mm)



PDFN3x3-8LC		
Dimension	Min.	Max.
A	2.90	3.10
B	2.90	3.10
C	0.55	0.75
D	0.20	0.40
E	0.00	0.20
F	0.00	0.20
G	0.55	0.75
H	0.20	0.40
I	0.20	0.40
J	0.10	0.20
K	3.15	3.45
L	0.25	0.45
M/O	0.90	1.20
N/P	1.65	1.85
Q	0.10	0.30

Mounting Pad Layout (Unit: mm)

PDFN3x3-8LC

