

### Features

- Low on-resistance
- Low input capacitance
- Fast switching speed
- HBM: JESD22-A114-B: 2

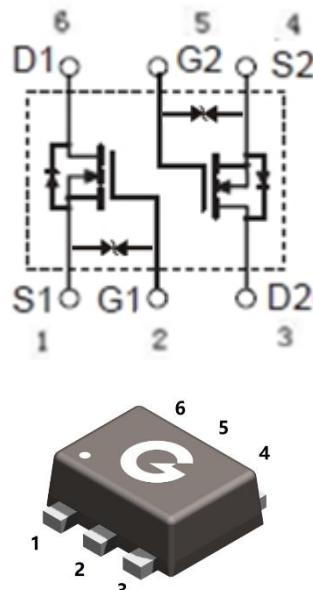
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### Typical Applications

- DC-DC converters
- Power management functions
- Battery operated systems and solid-state relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

### Mechanical Data

- Case: SOT-563
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin-Plated Leads, Solderability-per MIL-STD-202, Method 208



SOT-563

### Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
BSS138ESV	SOT-563	3000 pcs / Tape & Reel	MM5

### Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value		Unit
Drain-to-Source Voltage	$V_{DSS}$	50		V
Gate-to-Source Voltage	$V_{GSS}$	$\pm 20$		V
Continuous Drain Current ( $T_A = 25^\circ\text{C}$ ) *1	$I_D$	360		mA
Continuous Drain Current ( $T_A = 70^\circ\text{C}$ ) *1		290		mA
Pulsed Drain Current ( $t_p = 10\mu\text{s}$ , $T_A = 25^\circ\text{C}$ )	$I_{DM}$	1500		mA
Single Pulse Avalanche Energy *3	$E_{AS}$	0.2		mJ
Power Dissipation ( $T_A = 25^\circ\text{C}$ ) *1	$P_D$	250		mW
Operating Junction Temperature Range	$T_J$	-55 ~ +150		°C
Storage Temperature Range	$T_{STG}$	-55 ~ +150		°C

### Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Air *1	$R_{\theta JA}$	-	-	500	°C/W

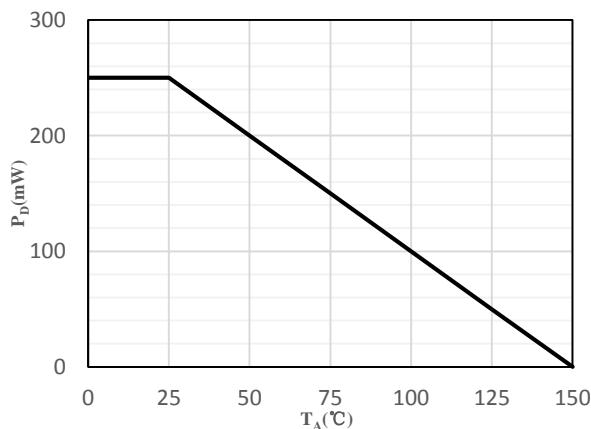
**Electrical Characteristics** (@  $T_A = 25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
$V_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}$ , $I_D = 250\mu\text{A}$	50	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 50\text{V}$ , $V_{GS} = 0\text{V}$	-	-	1	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS} = \pm 20\text{V}$ , $V_{DS} = 0\text{V}$	-	-	$\pm 10$	$\mu\text{A}$
<b>On Characteristics</b>						
$R_{DS(ON)}$	Drain-Source On-resistance <sup>*2</sup>	$V_{GS} = 10\text{V}$ , $I_D = 0.5\text{A}$	-	1	1.6	$\Omega$
		$V_{GS} = 4.5\text{V}$ , $I_D = 0.2\text{A}$	-	1.2	2.5	
		$V_{GS} = 2.5\text{V}$ , $I_D = 0.1\text{A}$	-	1.7	4.5	
$V_{GS(TH)}$	Static Drain-Source On-resistance	$V_{DS} = V_{GS}$ , $I_D = 250\mu\text{A}$	0.8	1	1.5	V
$R_G$	Gate Resistance	$V_{GS} = 0\text{V}$ , $f = 1\text{MHz}$	-	48	-	$\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}$	-	32	-	$\text{pF}$
$C_{oss}$	Output Capacitance		-	6	-	
$C_{rss}$	Reverse Transfer Capacitance		-	3	-	
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time <sup>*4</sup>	$V_{DD} = 25\text{V}$ , $I_D = 0.36\text{A}$	-	2.2	-	$\text{ns}$
$t_r$	Turn-on Rise Time <sup>*4</sup>		-	19.2	-	
$t_{d(off)}$	Turn-Off Delay Time <sup>*4</sup>		-	6.2	-	
$t_f$	Turn-Off Fall Time <sup>*4</sup>		-	23	-	
$Q_G$	Total Gate-Charge	$V_{DS} = 25\text{V}$	-	4	-	$\text{nC}$
$Q_{GS}$	Gate to Source Charge		-	0.5	-	
$Q_{GD}$	Gate to Drain (Miller) Charge		-	0.4	-	
<b>Source-Drain Diode Characteristics</b>						
$V_{SD}$	Diode Forward Voltage <sup>*2</sup>	$I_S = 0.5\text{A}$ , $V_{GS} = 0\text{V}$	-	0.89	1.4	V
$trr$	Reverse Recovery Time	$I_F = 1\text{A}$ , $V_{GS} = 0\text{V}$	-	15	-	$\text{ns}$
$Qrr$	Reverse Recovery Charge		-	8	-	$\text{nC}$

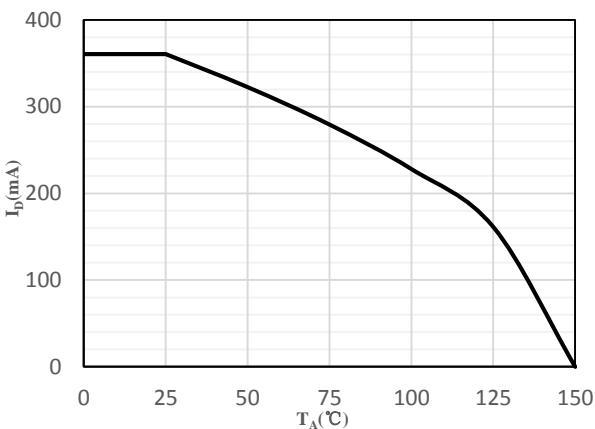
Notes:

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper
2. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$
3. The  $E_{AS}$  data shows Max. rating. The test condition is  $V_{DD} = 30\text{V}$ ,  $V_{GS} = 10\text{V}$ ,  $L = 0.5\text{mH}$
4. Guaranteed by design, not subject to production

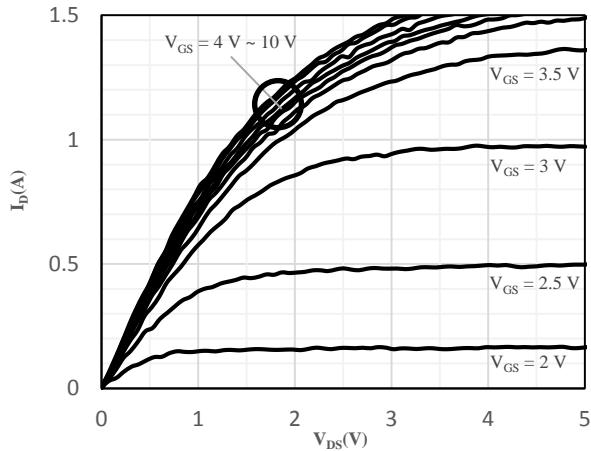
### Ratings and Characteristic Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)



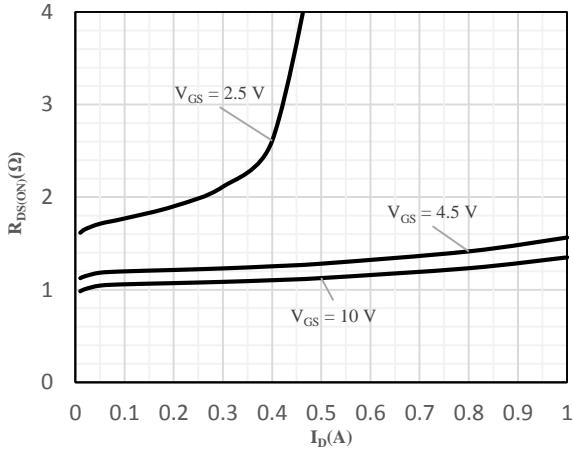
**Fig 1 Power Dissipation**



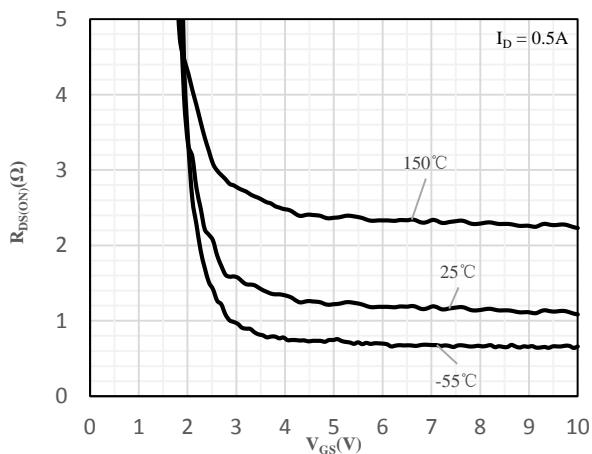
**Fig 2 Drain Current**



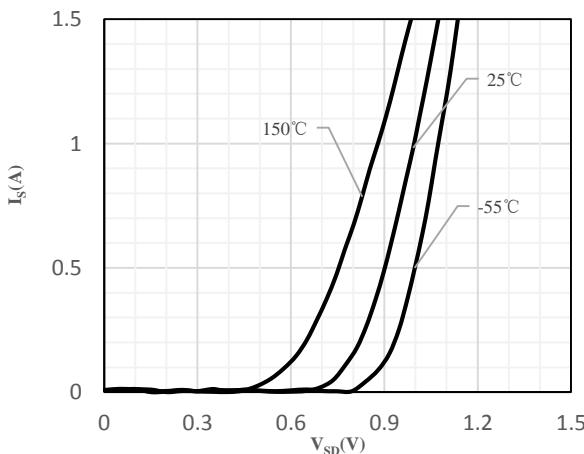
**Fig 3 Typical Output Characteristics**



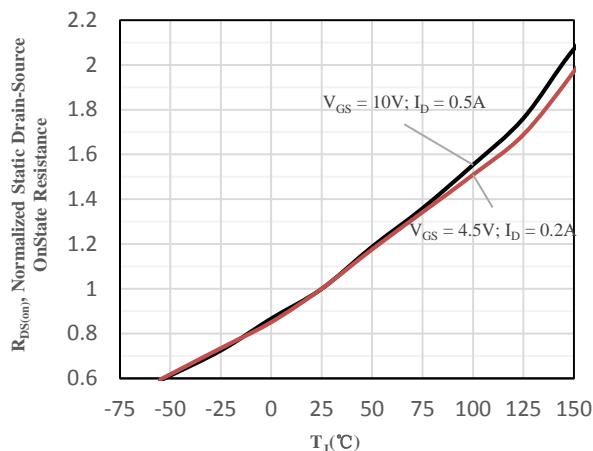
**Fig 4 On-Resistance vs. Drain Current  
and Gate Voltage**



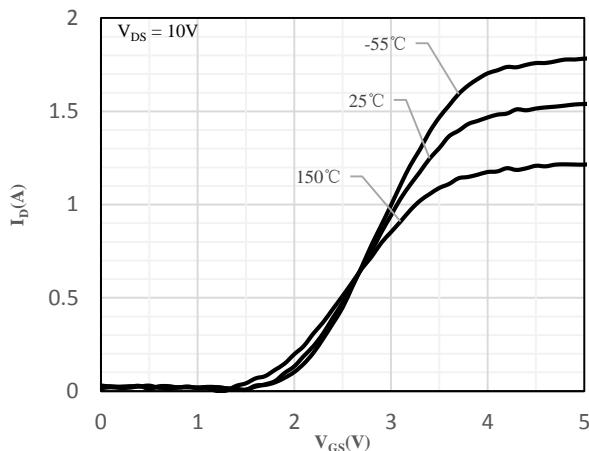
**Fig 5 On-Resistance vs. Gate-Source Voltage**



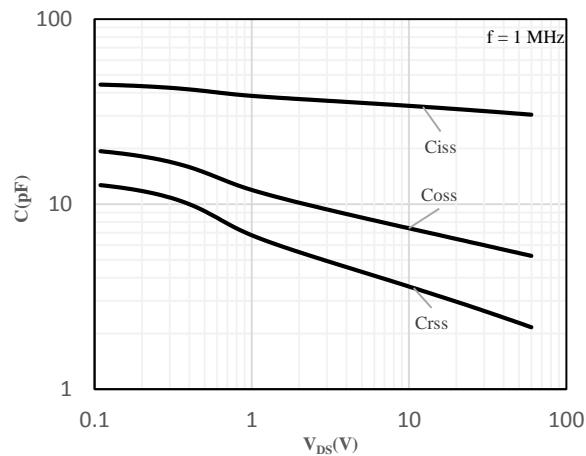
**Fig 6 Body-Diode Characteristics**



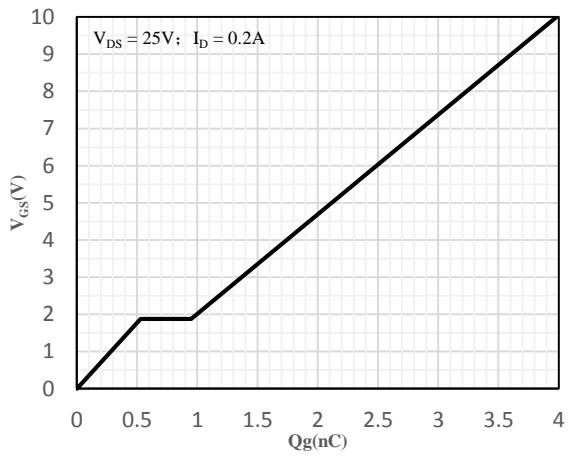
**Fig 7 Normalized On-Resistance vs. Junction Temperature**



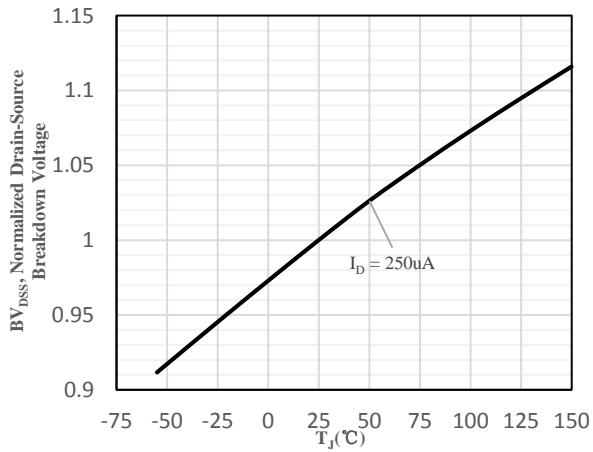
**Fig 8 Transfer Characteristics**



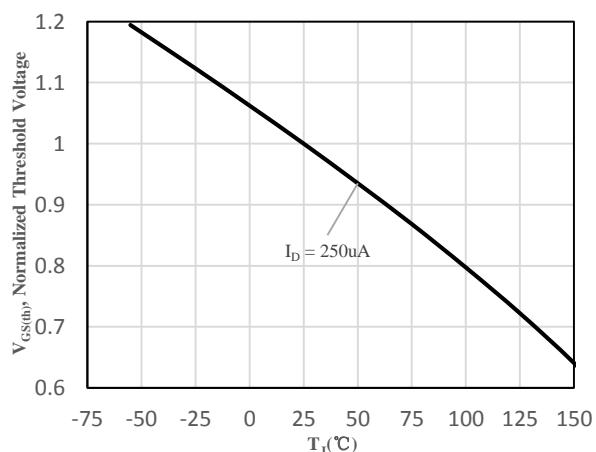
**Fig 9 Capacitance Characteristics**



**Fig 10 Gate-Charge Characteristics**

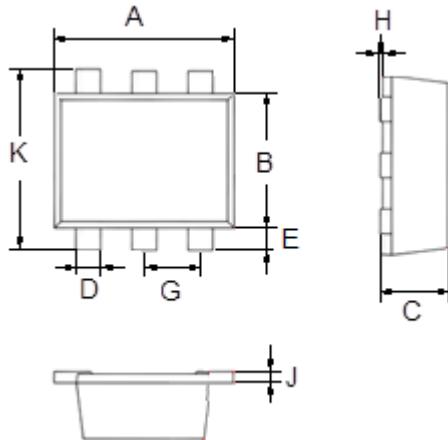


**Fig 11 Normalized Breakdown Voltage vs. Junction Temperature**



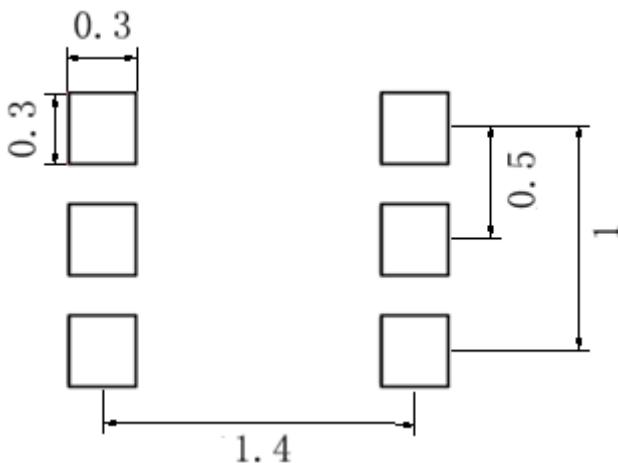
**Fig 12 Normalized  $V_{GS(th)}$  vs. Junction Temperature**

### Package Outline Dimensions (Unit: mm)



SOT-563		
Dimension	Min.	Max.
A	1.500	1.700
B	1.100	1.300
C	0.525	0.600
D	0.170	0.270
E	0.100	0.300
G	0.450	0.550
H	0.000	0.050
J	0.090	0.160
K	1.500	1.700

### Mounting Pad Layout (Unit: mm)

**SOT-563**


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