

Features

- Low forward voltage drop
- High breakdown voltage
- Guard ring protected
- Low capacitance
- Very small SMD package
- RoHS compliant with Halogen-free
- Qualified to AEC-Q101 Standards

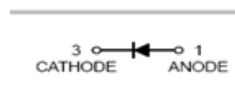
HF

Applications

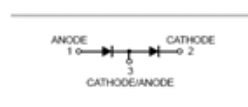
- Schottky barrier detector and switching diodes

Mechanical Data

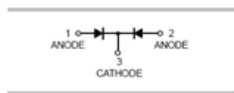
- Case: SOT-323
- Molding compound: UL flammability classification rating 94V-0
- Terminals: Tin-plated; solderability per MIL-STD-202, Method 208



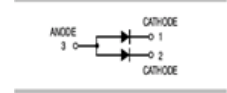
TBAS70W



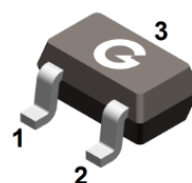
TBAS70W-04



TBAS70W-05



TBAS70W-06



SOT-323

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
TBAS70W	SOT-323	3000 pcs / Tape & Reel	K73
TBAS70W-04	SOT-323	3000 pcs / Tape & Reel	K74
TBAS70W-05	SOT-323	3000 pcs / Tape & Reel	K75
TBAS70W-06	SOT-323	3000 pcs / Tape & Reel	K76

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	70	V
Working Peak Reverse Voltage	V _{RWM}	70	V
DC Reverse Voltage	V _R	70	V
RMS Reverse Voltage	V _{R(RMS)}	49	V
Continuous Forward Current	I _F	70	mA
Non-Repetitive Peak Forward Surge Current (t _p = 8.3ms)	I _{FSM}	0.1	A

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation ($T_A = 25^\circ\text{C}$)	P_D	200	mW
Thermal Resistance Junction-to-Air ^{*1}	$R_{\theta JA}$	390	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Case ^{*1}	$R_{\theta JC}$	275	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Lead ^{*1}	$R_{\theta JL}$	350	$^\circ\text{C/W}$
Operating Junction Temperature Range	T_J	-55 ~ +125	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Reverse Breakdown Voltage	$V_{(BR)R}$	$I_R = 10\mu\text{A}$	70	-	-	V
Forward Voltage ^{*2}	V_F	$I_F = 1\text{mA}$	-	-	0.41	V
		$I_F = 15\text{mA}$	-	-	1	V
Maximum Peak Reverse Current ^{*3}	I_R	$V_R = 50\text{V}$	-	-	100	nA
Capacitance Between Terminals	C_J	$V_R = 0\text{V}, f = 1\text{MHz}$	-	-	2	pF
Reverse Recovery Time	t_{rr}	$I_F = I_R = 10\text{mA}$ $I_{rr} = 0.1I_R, R_L = 100\Omega$	-	-	5	ns

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- Pulse test, $t_p \leq 300\mu\text{s}$
- Pulse test, $t_p \leq 5\text{ms}$

Ratings and Characteristic Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

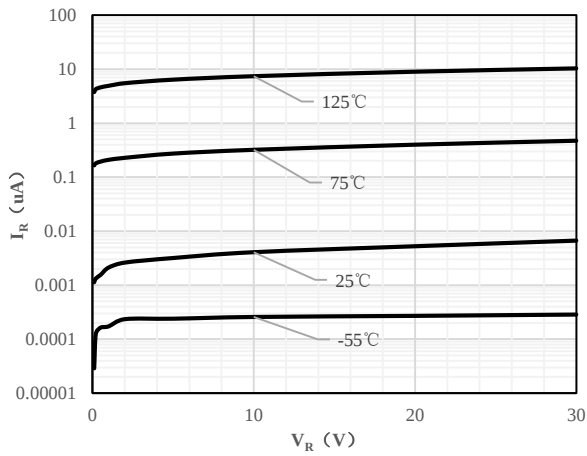


Fig 1 Typical Reverse Characteristic

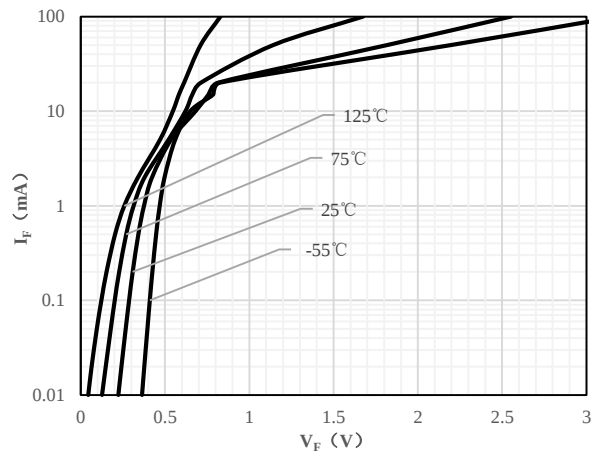


Fig 2 Typical Forward Characteristics

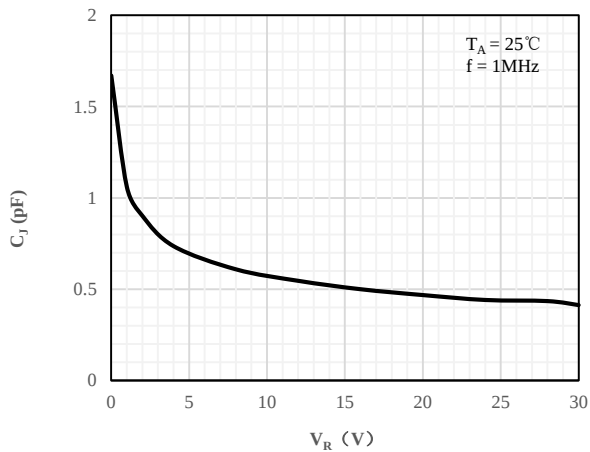


Fig 3 Capacitance Characteristics

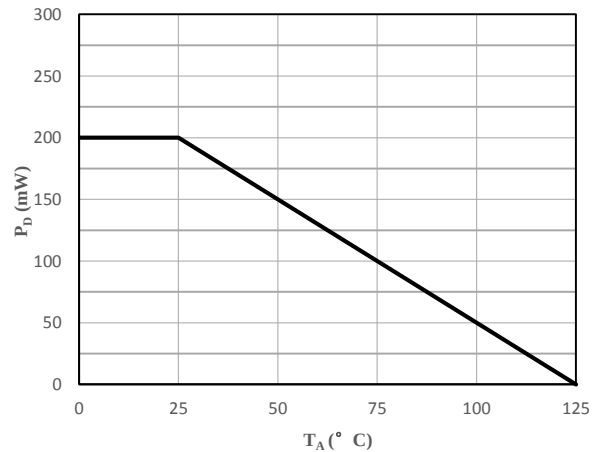
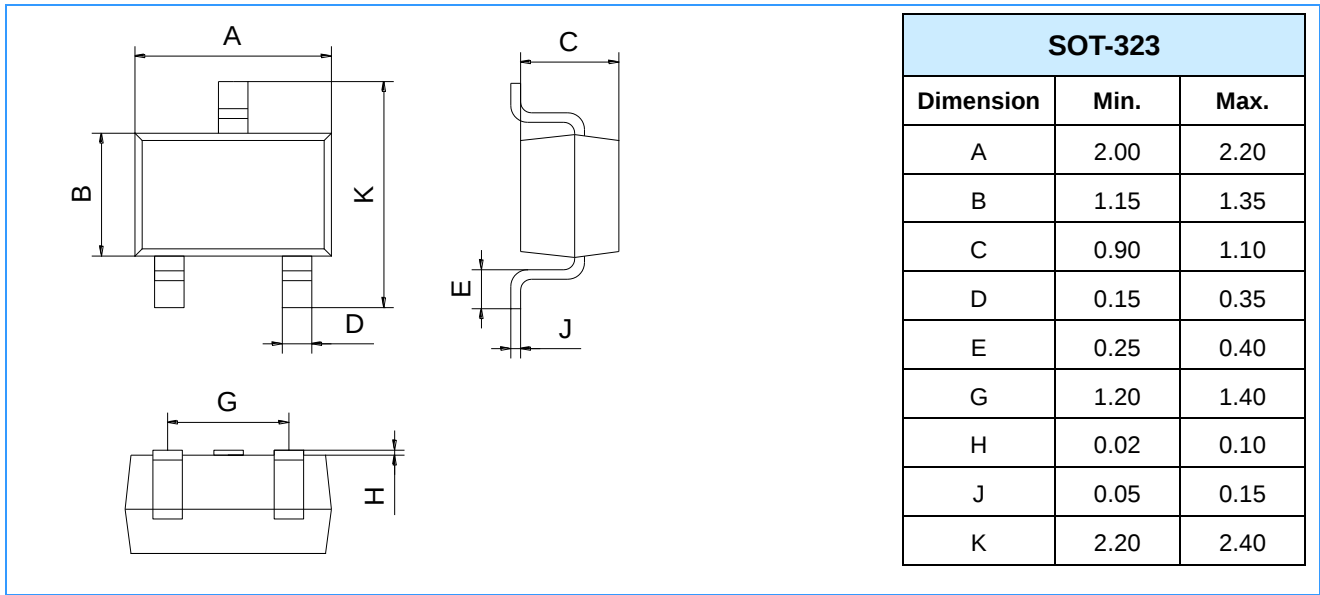


Fig 4 Power Derating Curve

Package Outline Dimensions (Unit: mm)



Mounting Pad Layout (Unit: mm)

